

UT6 BLOCK DIAGRAM

LAYER 1 : TOP
LAYER 2 : SGND
LAYER 3 : IN1
LAYER 4 : SVCC
LAYER 5 : IN2
LAYER 6 : IN3
LAYER 7 : SGND1
LAYER 8 : BOT

Cable Docking

VGA
 RJ-45
 CIR/Pwr btn
 SPDIF Out
 Stereo MIC
 Headphone Jack
 USB Port
 VOL Cntr

PAGE 38

SYSTEM CHARGER ISL6251AHAZ-
 PAGE 39

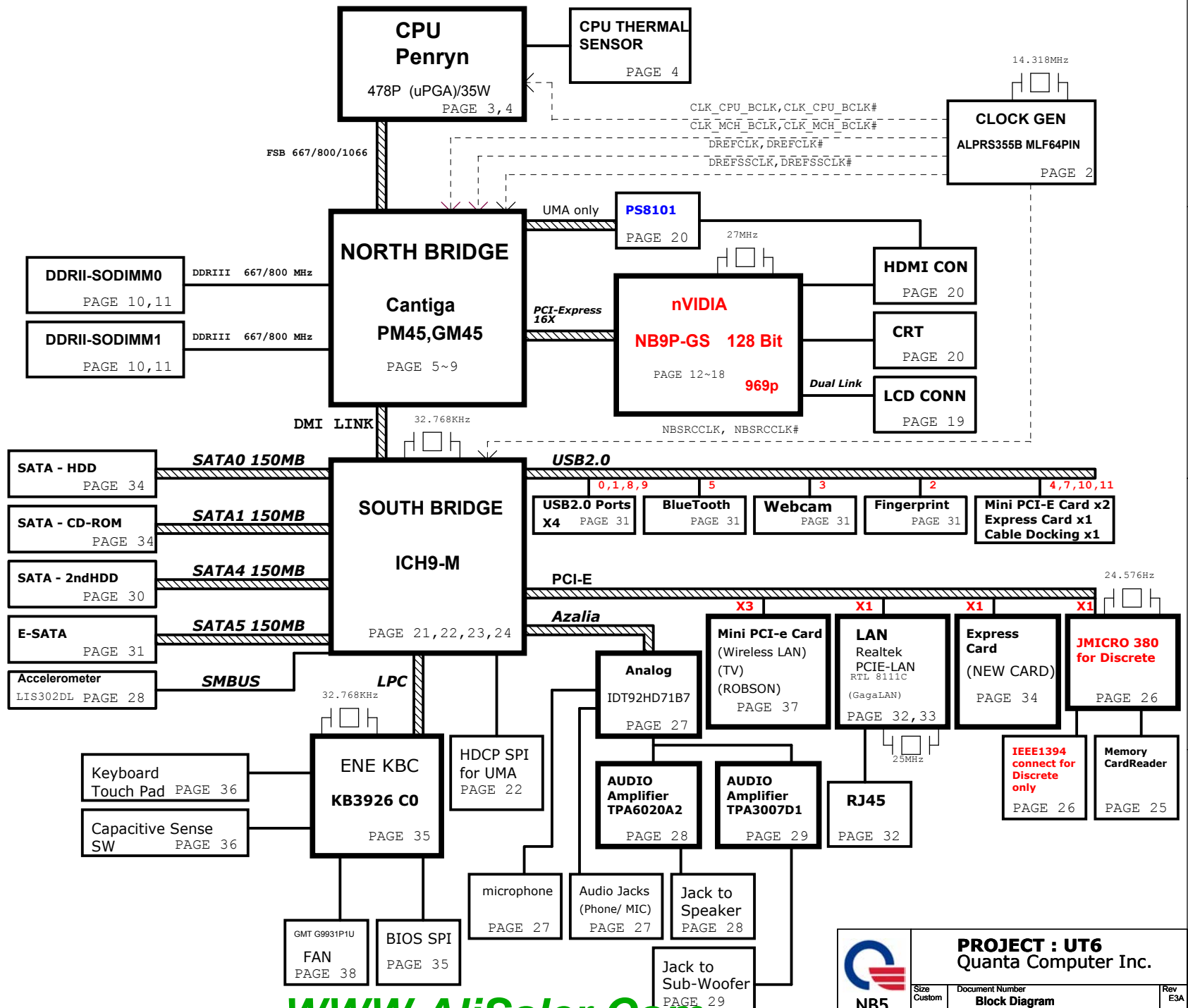
SYSTEM POWER ISL6237IRZ-T
 PAGE 40

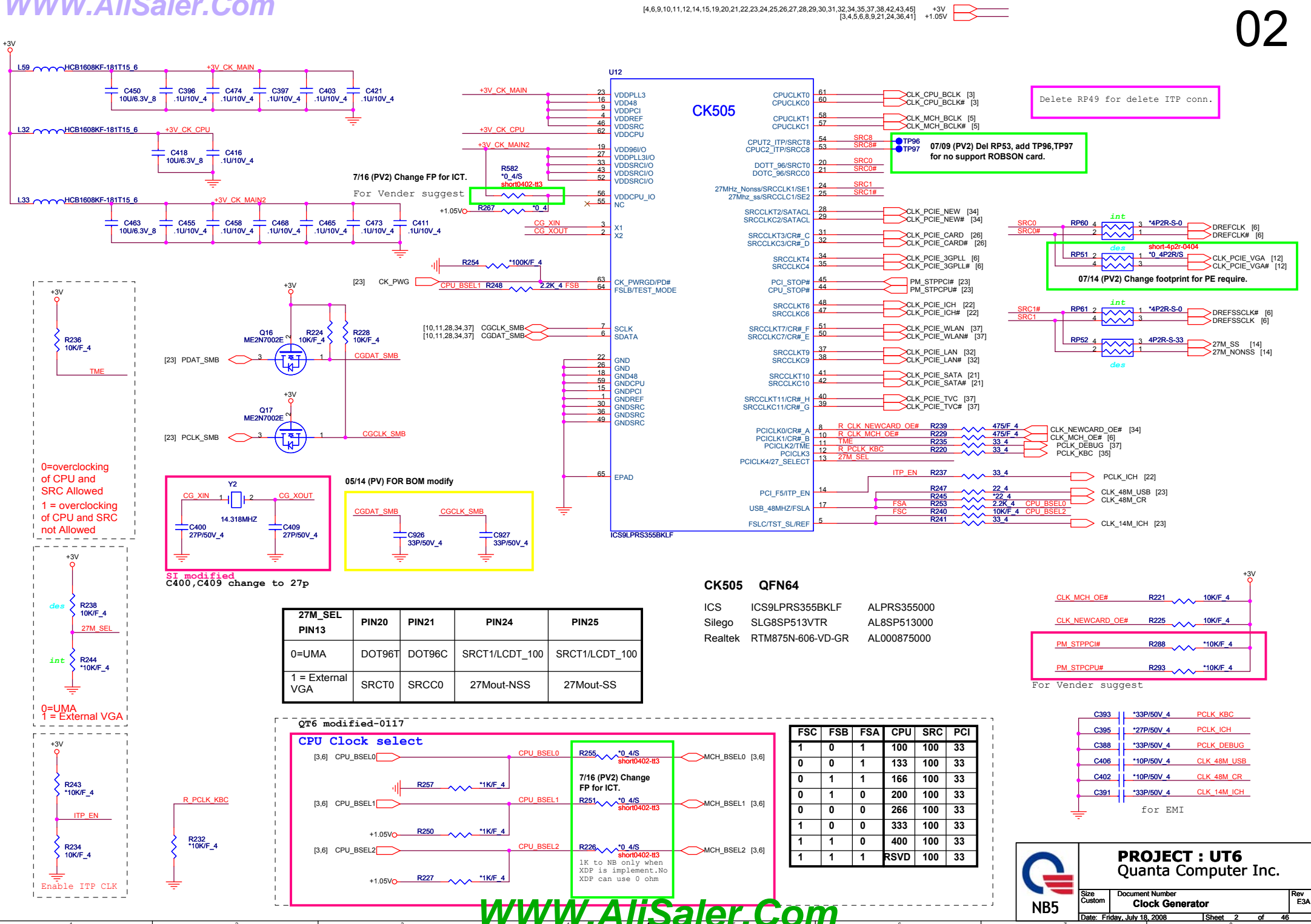
DDR II SMDDR_VTERM
 1.8V/1.8VSUS(TPS51116REGR)
 PAGE 44

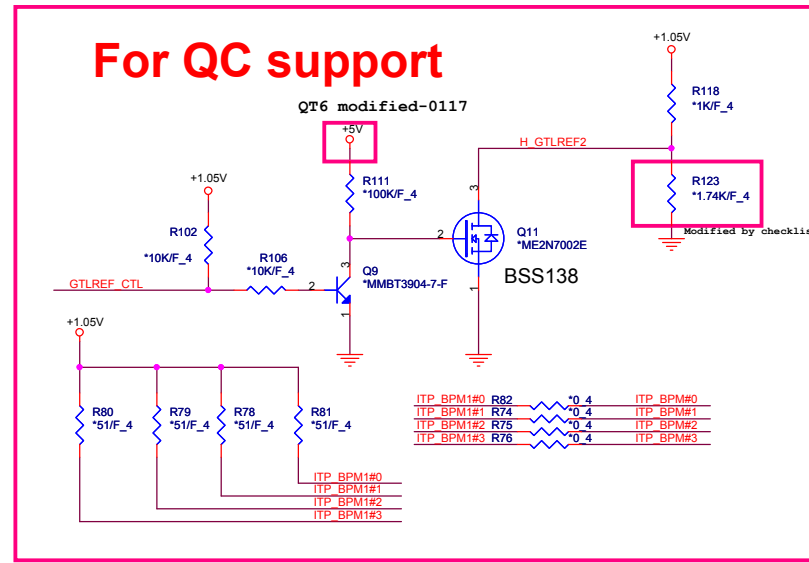
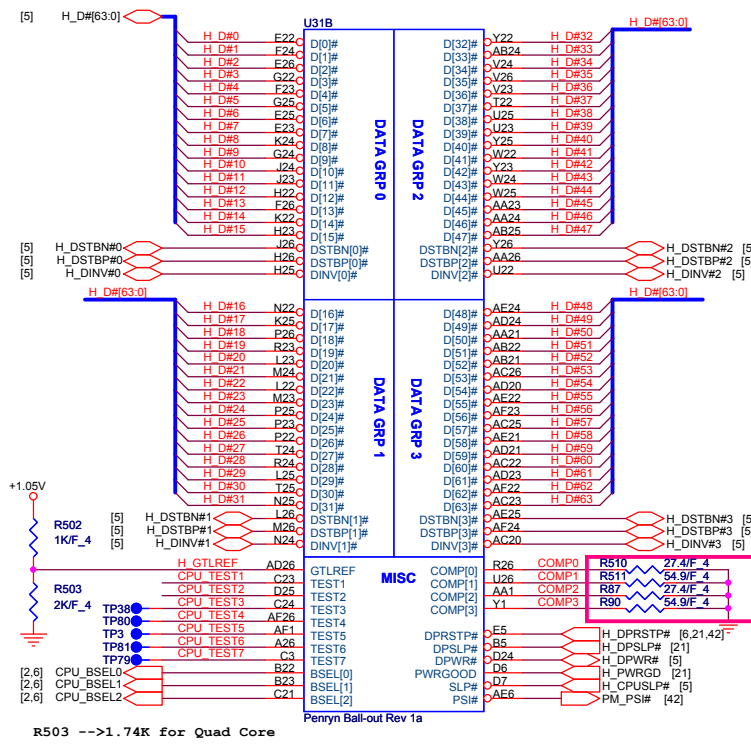
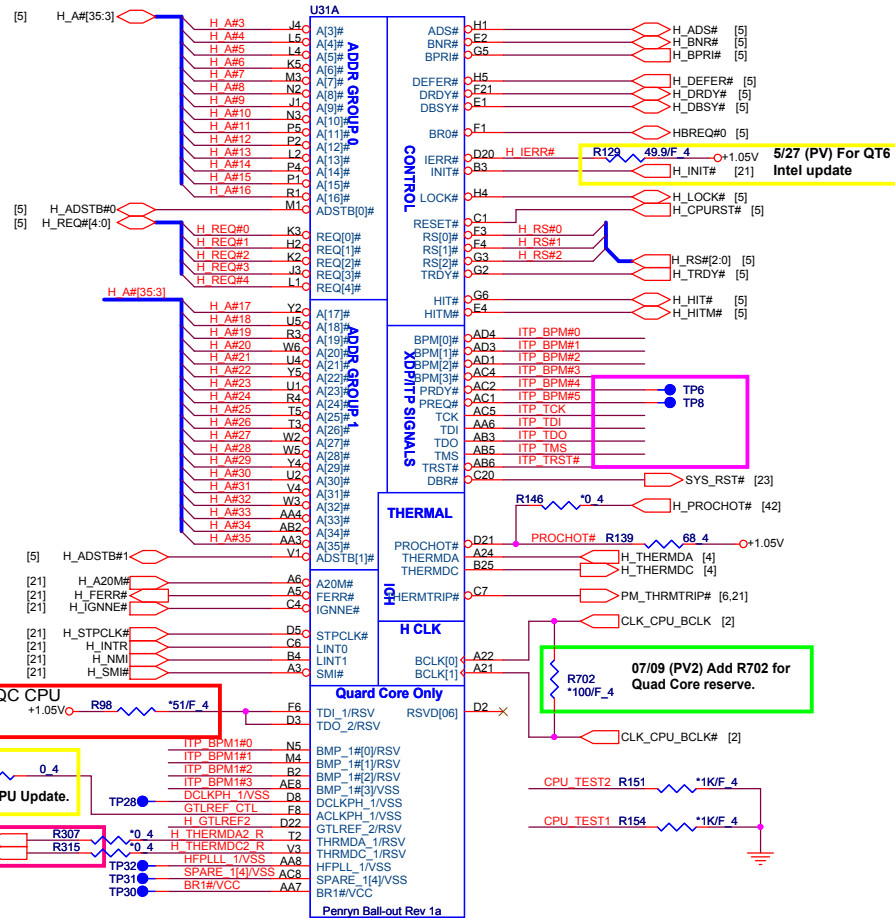
VCCP +1.5V AND GMCH
 1.05V(RT8204)
 PAGE 44

VGACORE(1.025V)Oz8118
 PAGE 43

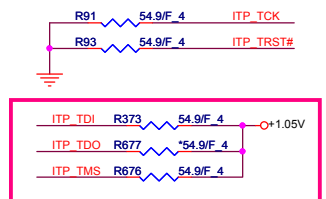
CPU CORE ISL6266A
 PAGE 42





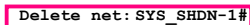
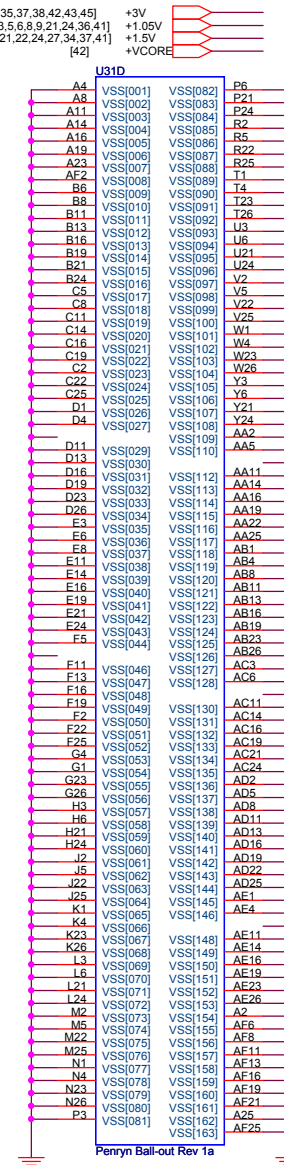


Populate ITP700Flex for bringup



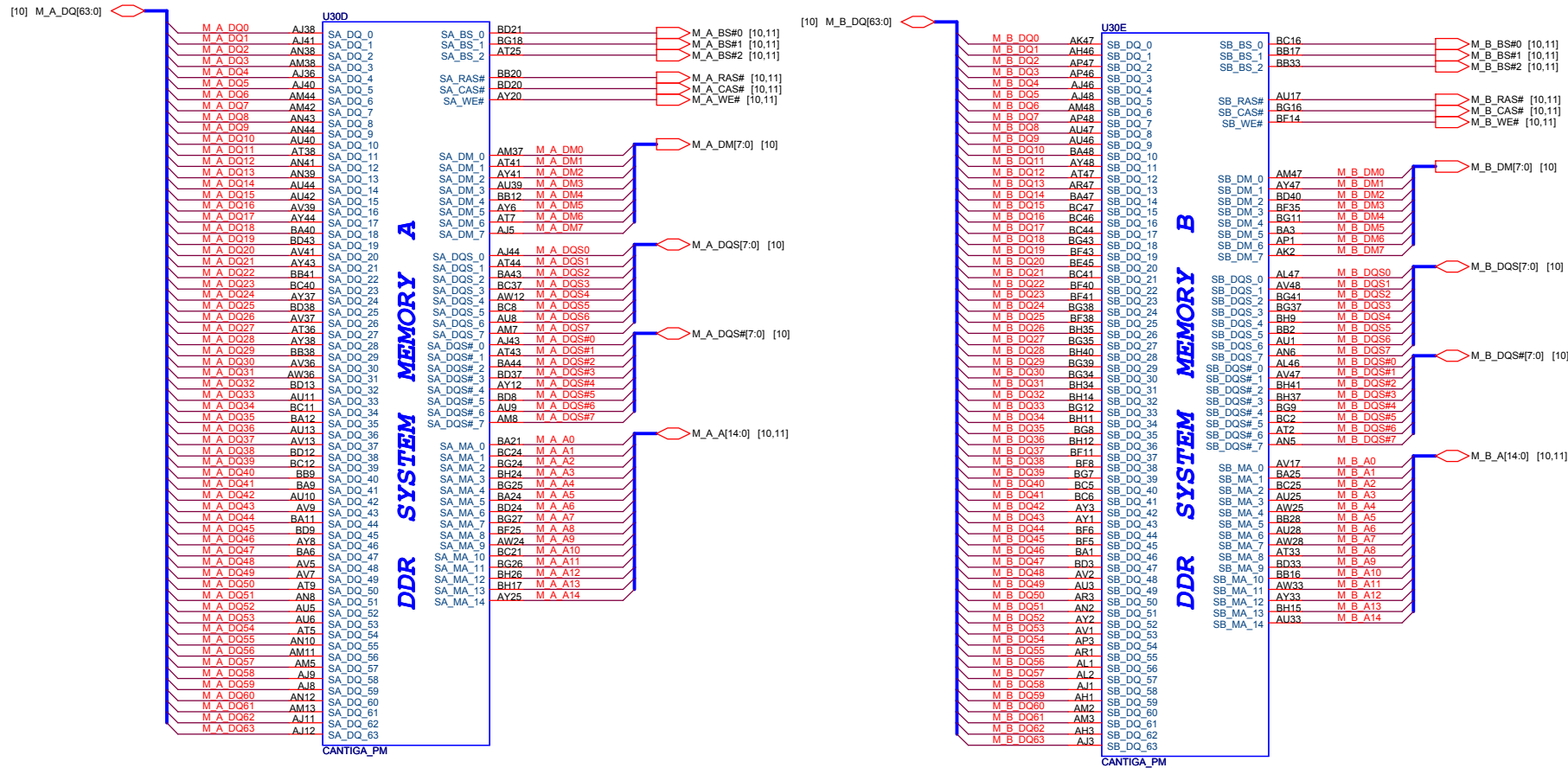
PROJECT : UT6
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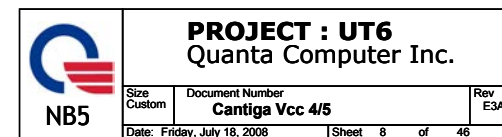
Size Custom Document Number **Penryn 1/2** Rev E3A
Date: Friday, July 18, 2008 Sheet 3 of 46

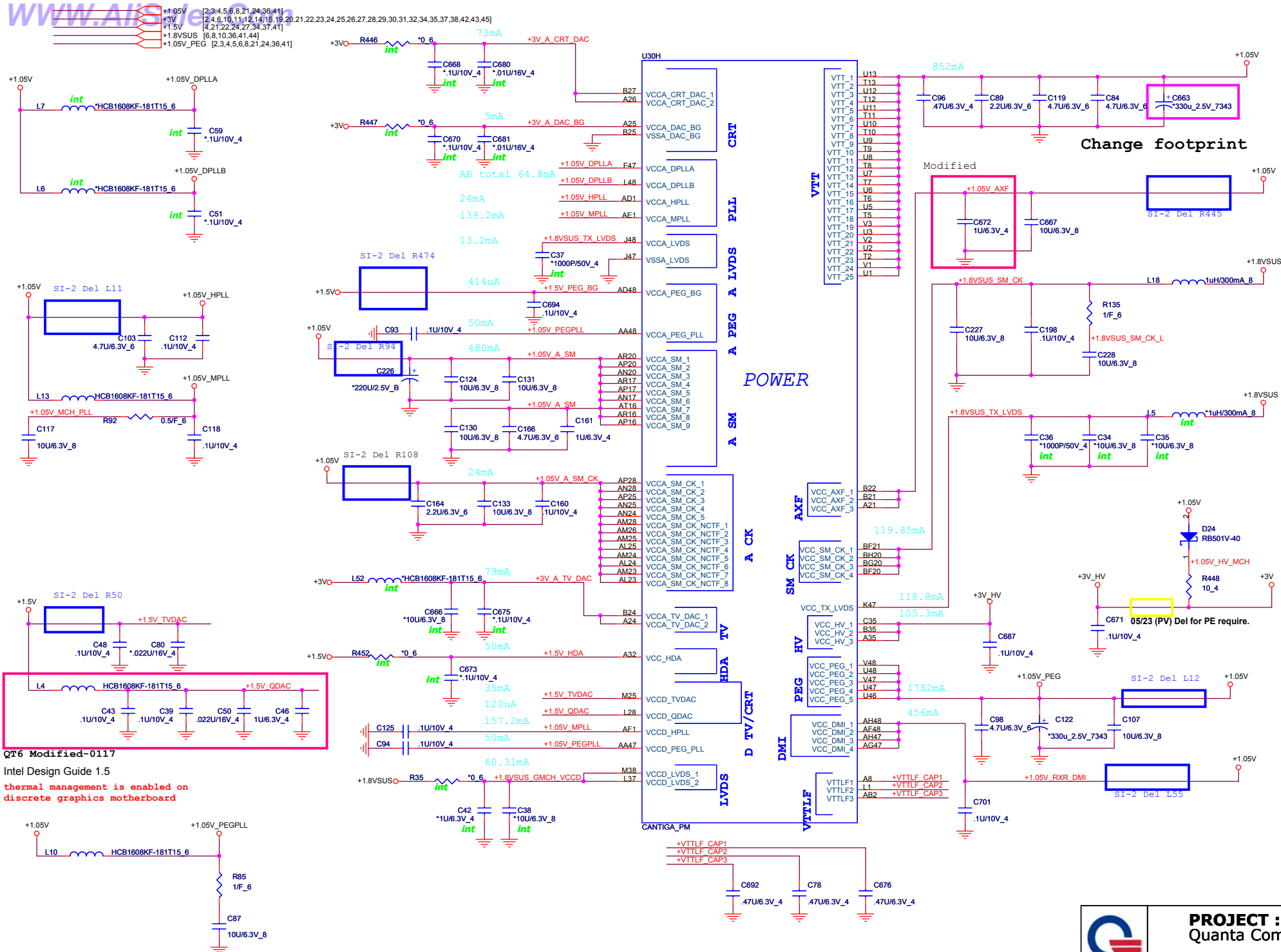


QT6 Modified-0117





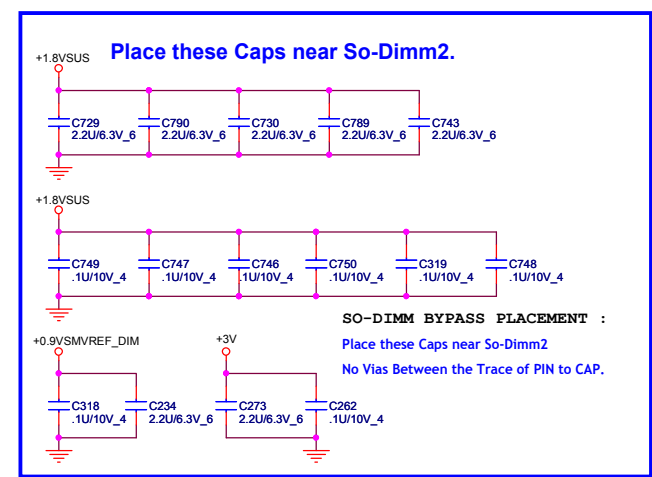
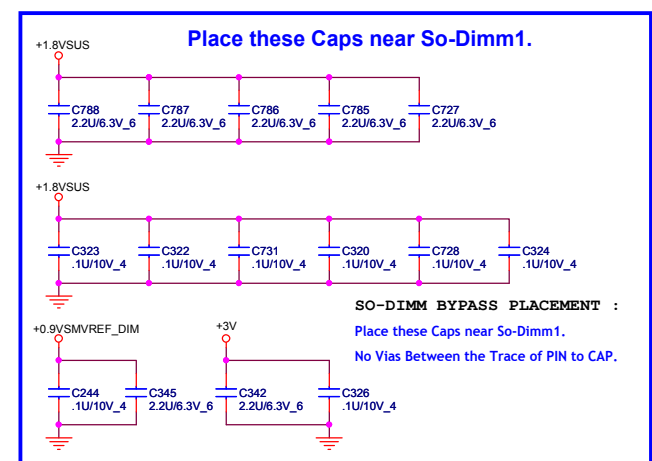
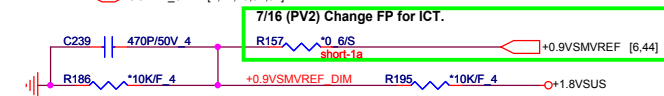
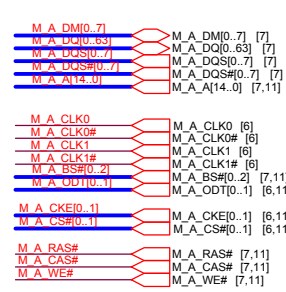
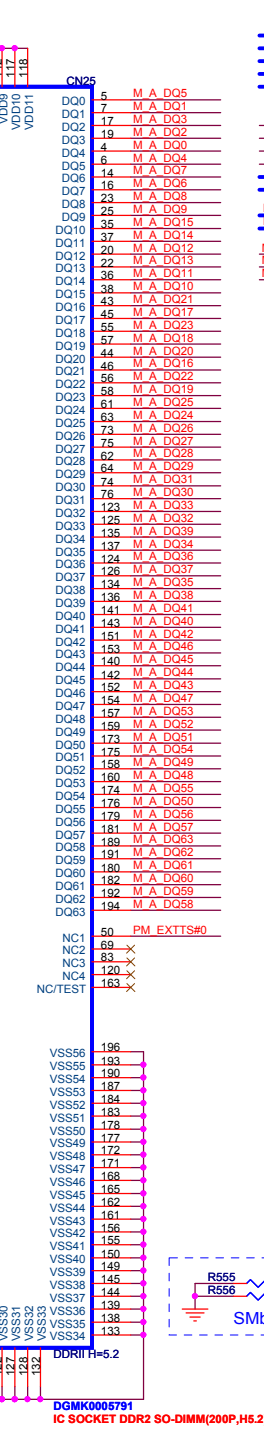




QT6 Modified-0117
Intel Design Guide 1.5
thermal management is enabled on
discrete graphics motherboard

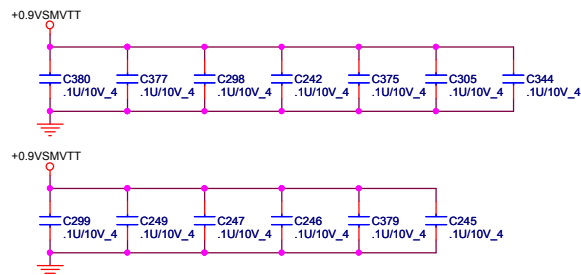
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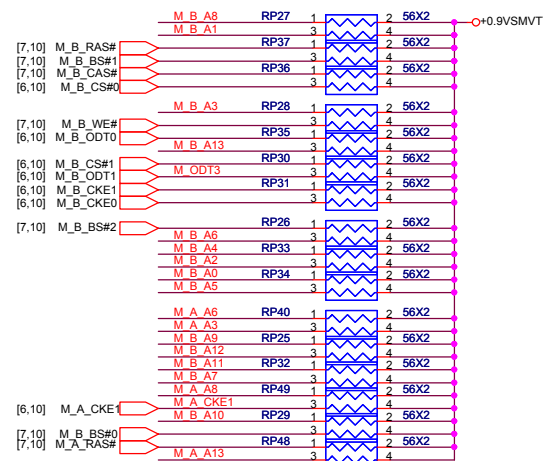
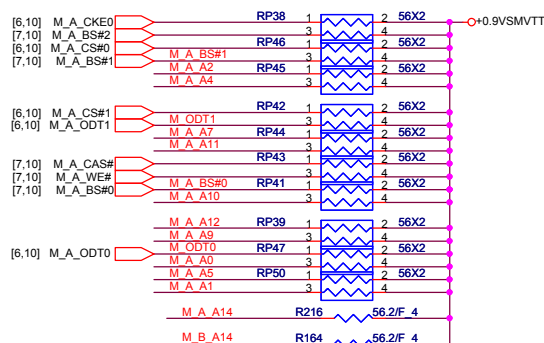
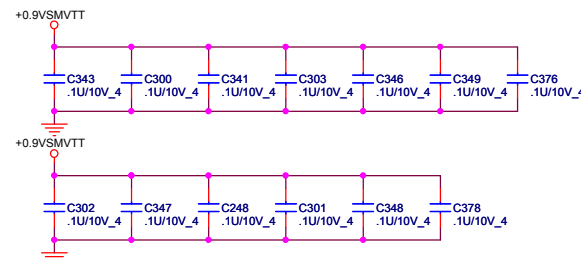
DDRII DUAL CHANNEL A,B.

DDRII A CHANNEL

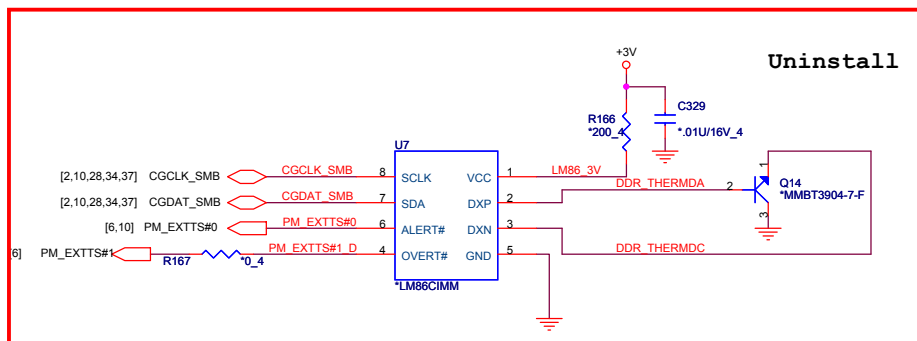


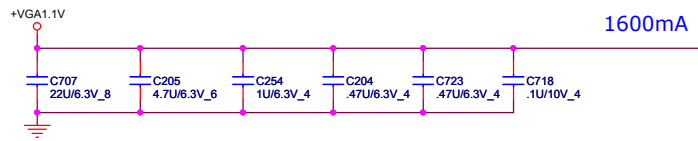
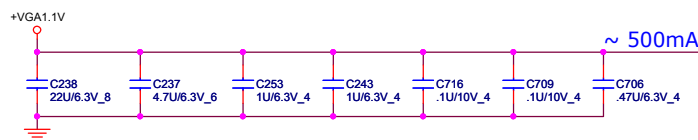
Layout note: Place one cap close to every 2 pullup resistors terminated to SMDR_VTERM

DDRII B CHANNEL

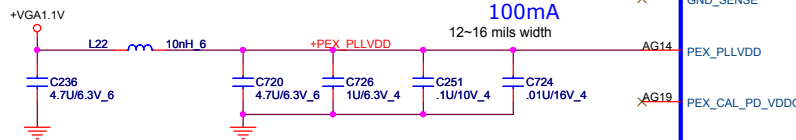
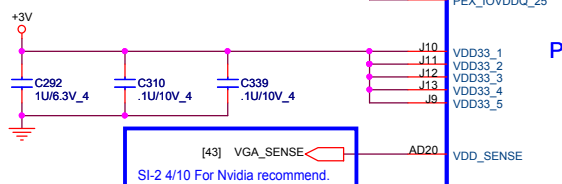


M_B_A[14..0] M_B_A[14..0] [7,10]
M_A_A[14..0] M_A_A[14..0] [7,10]





Near BGA



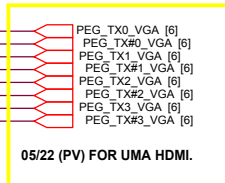
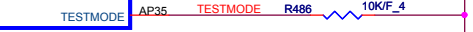
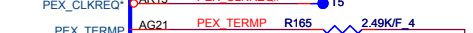
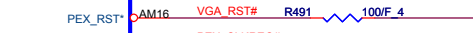
U35A
BGA689-NVIDIA-NB8P-GS
COMMON

- AK16 PEX_IOVDD_1
- AK17 PEX_IOVDD_2
- AK21 PEX_IOVDD_3
- AK24 PEX_IOVDD_4
- AK27 PEX_IOVDD_5
- AG11 PEX_IOVDDQ_1
- AG12 PEX_IOVDDQ_2
- AG13 PEX_IOVDDQ_3
- AG15 PEX_IOVDDQ_4
- AG16 PEX_IOVDDQ_5
- AG17 PEX_IOVDDQ_6
- AG18 PEX_IOVDDQ_7
- AG22 PEX_IOVDDQ_8
- AG23 PEX_IOVDDQ_9
- AG24 PEX_IOVDDQ_10
- AG25 PEX_IOVDDQ_11
- AG26 PEX_IOVDDQ_12
- AG27 PEX_IOVDDQ_13
- AG14 PEX_IOVDDQ_14
- AG15 PEX_IOVDDQ_15
- AG19 PEX_IOVDDQ_16
- AG21 PEX_IOVDDQ_17
- AG24 PEX_IOVDDQ_18
- AG25 PEX_IOVDDQ_19
- AG27 PEX_IOVDDQ_20
- AK18 PEX_IOVDDQ_21
- AK20 PEX_IOVDDQ_22
- AK23 PEX_IOVDDQ_23
- AK26 PEX_IOVDDQ_24
- AL16 PEX_IOVDDQ_25

PCI EXPRESS

- PEX_TX0 AL17 C PEG RX0 C171 .1U/10V_4
- PEX_TX0* AM17 C PEG RX#0 C170 .1U/10V_4
- PEX_TX1 AM18 C PEG RX1 C209 .1U/10V_4
- PEX_TX1* AM19 C PEG RX#1 C210 .1U/10V_4
- PEX_TX2 AL19 C PEG RX2 C172 .1U/10V_4
- PEX_TX2* AK19 C PEG RX#2 C173 .1U/10V_4
- PEX_TX3 AL20 C PEG RX3 C145 .1U/10V_4
- PEX_TX3* AM20 C PEG RX#3 C146 .1U/10V_4
- PEX_TX4 AM21 C PEG RX4 C211 .1U/10V_4
- PEX_TX4* AM22 C PEG RX#4 C212 .1U/10V_4
- PEX_TX5 AL22 C PEG RX5 C213 .1U/10V_4
- PEX_TX5* AK22 C PEG RX#5 C214 .1U/10V_4
- PEX_TX6 AL23 C PEG RX6 C147 .1U/10V_4
- PEX_TX6* AM23 C PEG RX#6 C148 .1U/10V_4
- PEX_TX7 AM24 C PEG RX7 C174 .1U/10V_4
- PEX_TX7* AK25 C PEG RX#7 C175 .1U/10V_4
- PEX_TX8 AL25 C PEG RX8 C149 .1U/10V_4
- PEX_TX8* AK26 C PEG RX#8 C150 .1U/10V_4
- PEX_TX9 AM26 C PEG RX9 C176 .1U/10V_4
- PEX_TX9* AM27 C PEG RX#9 C177 .1U/10V_4
- PEX_TX10 AL28 C PEG RX10 C151 .1U/10V_4
- PEX_TX10* AM28 C PEG RX#10 C152 .1U/10V_4
- PEX_TX11 AL28 C PEG RX11 C199 .1U/10V_4
- PEX_TX11* AK28 C PEG RX#11 C200 .1U/10V_4
- PEX_TX12 AM29 C PEG RX12 C178 .1U/10V_4
- PEX_TX12* AK29 C PEG RX#12 C179 .1U/10V_4
- PEX_TX13 AL29 C PEG RX13 C162 .1U/10V_4
- PEX_TX13* AM30 C PEG RX#13 C163 .1U/10V_4
- PEX_TX14 AM31 C PEG RX14 C155 .1U/10V_4
- PEX_TX14* AK32 C PEG RX#14 C156 .1U/10V_4
- PEX_TX15 AL32 C PEG RX15 C180 .1U/10V_4
- PEX_TX15* AK32 C PEG RX#15 C181 .1U/10V_4

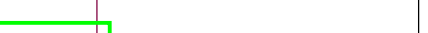
- PEX_REFCLK AR16 CLK_PCIE_VGA
- PEX_REFCLK* AR17 CLK_PCIE_VGA#



- PEG_TX0_VGA [6]
- PEG_TX#0_VGA [6]
- PEG_TX1_VGA [6]
- PEG_TX#1_VGA [6]
- PEG_TX2_VGA [6]
- PEG_TX#2_VGA [6]
- PEG_TX3_VGA [6]
- PEG_TX#3_VGA [6]

- PEG_RX0 [6]
- PEG_RX#0 [6]
- PEG_RX1 [6]
- PEG_RX#1 [6]
- PEG_RX2 [6]
- PEG_RX#2 [6]
- PEG_RX3 [6]
- PEG_RX#3 [6]
- PEG_RX4 [6]
- PEG_RX#4 [6]
- PEG_RX5 [6]
- PEG_RX#5 [6]
- PEG_RX6 [6]
- PEG_RX#6 [6]
- PEG_RX7 [6]
- PEG_RX#7 [6]
- PEG_RX8 [6]
- PEG_RX#8 [6]
- PEG_RX9 [6]
- PEG_RX#9 [6]
- PEG_RX10 [6]
- PEG_RX#10 [6]
- PEG_RX11 [6]
- PEG_RX#11 [6]
- PEG_RX12 [6]
- PEG_RX#12 [6]
- PEG_RX13 [6]
- PEG_RX#13 [6]
- PEG_RX14 [6]
- PEG_RX#14 [6]
- PEG_RX15 [6]
- PEG_RX#15 [6]

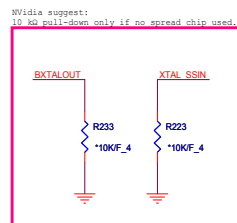
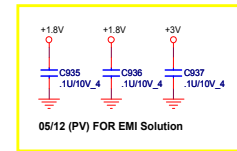
- CLK_PCIE_VGA [2]
- CLK_PCIE_VGA# [2]

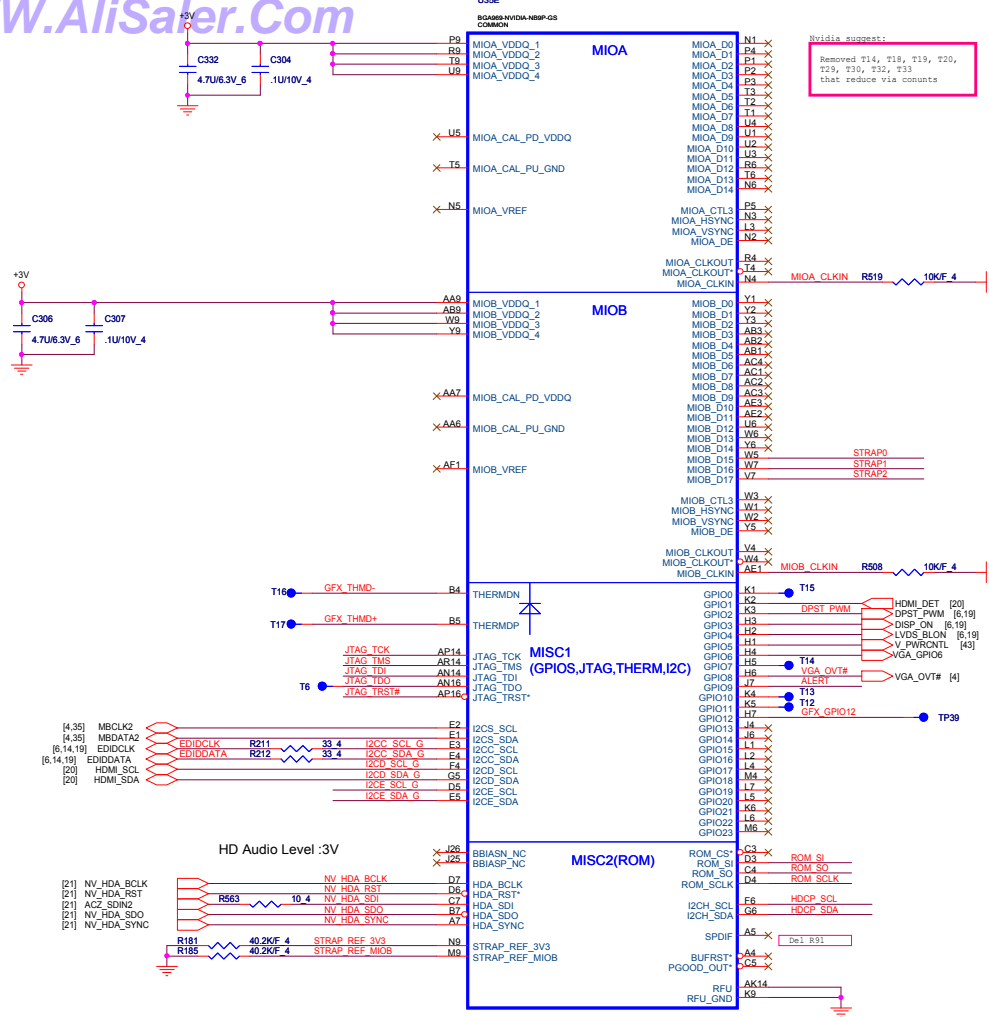


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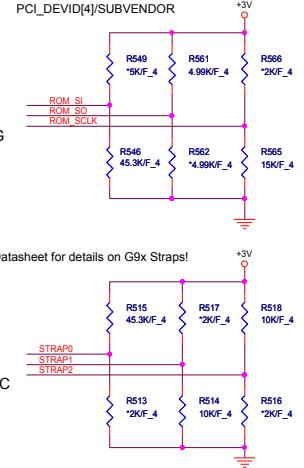






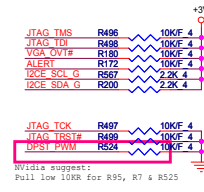
NB9P-GS (G96) Straps
NB9M-GE (G98) Straps
GPIO ASSIGNMENTS

GPIO	I/O	ACTIVE	USAGE
0	IN	N/A	PRIMARY DVI HOTPLUG
1	IN	N/A	SECONDARY DVI HOTPLUG
2	OUT	HIGH	PANEL BACKLIGHT PWM
3	OUT	HIGH	PANEL POWER ENABLE
4	OUT	HIGH	PANEL BACKLIGHT ENABLE
5	OUT	N/A	NVDD VID0
6	OUT	N/A	NVDD VID1
7	OUT	N/A	FBVDD VID0
8	IN	LOW	THERMAL ALERT
9	OUT	LOW	FAN PWM
10	OUT	N/A	FBVREF SELECT
11	OUT	N/A	SLI SYNC0
12	IN	N/A	AC DETECT
13	OUT	LOW	PS CONTROL OR HDMI_CEC
14	OUT	HIGH	PS CONTROL



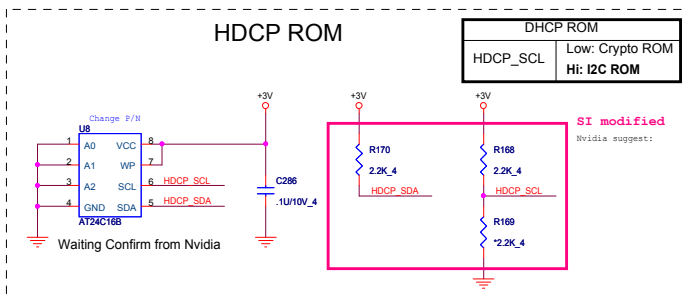
Logical Strap Bit Mapping

	PU-VDD	PD
5K	1000	0000
10K	1001	0001
15K	1010	0010
20K	1011	0011
25K	1100	0100
30K	1101	0101
35K	1110	0110
45K	1111	0111



	Logical Strapping Bit3	Logical Strapping Bit2	Logical Strapping Bit1	Logical Strapping Bit0
ROM_SO	XCLK_277	TVMODE[2]	TVMODE[1]	TVMODE[0]
ROM_SCLK	PCI_DEVIDE[4]	SUB_VENDOR	SLOT_CLK_CFG	PEX_PLL_EN_TERM100
ROM_SI	RAMCFG[3]	RAMCFG[2]	RAMCFG[1]	RAMCFG[0]
STRAP2	PCI_DEVID[3]	PCI_DEVID[2]	PCI_DEVID[1]	PCI_DEVID[0]
STRAP1	3GIO_PADCFG[3]	3GIO_PADCFG[2]	3GIO_PADCFG[1]	3GIO_PADCFG[0]
STRAP0	USER[3]	USER[2]	USER[1]	USER[0]

Delete VGA thermal circuit



NB9X VRAM Configuration Table

RAM_CFG[3:0]	DESCRIPTION	Vendor
0111	DDR2 32Mx16x8, 128bit, 512MB	Hynix
0110	DDR2 32Mx16x8, 128bit, 512MB	Qimonda
0101	DDR2 32Mx16x8, 128bit, 512MB	Samsung
0100	DDR2 32Mx16x8, 128bit, 512MB	Nanya/Elpida
0000	DDR2 64Mx16x8, 128bit, 1GB	Hynix
0001	DDR2 64Mx16x8, 128bit, 1GB	Qimonda
0010	DDR2 64Mx16x8, 128bit, 1GB	Samsung
0011	DDR2 64Mx16x8, 128bit, 1GB	Samsung

PCI_DEVID: STRAP2

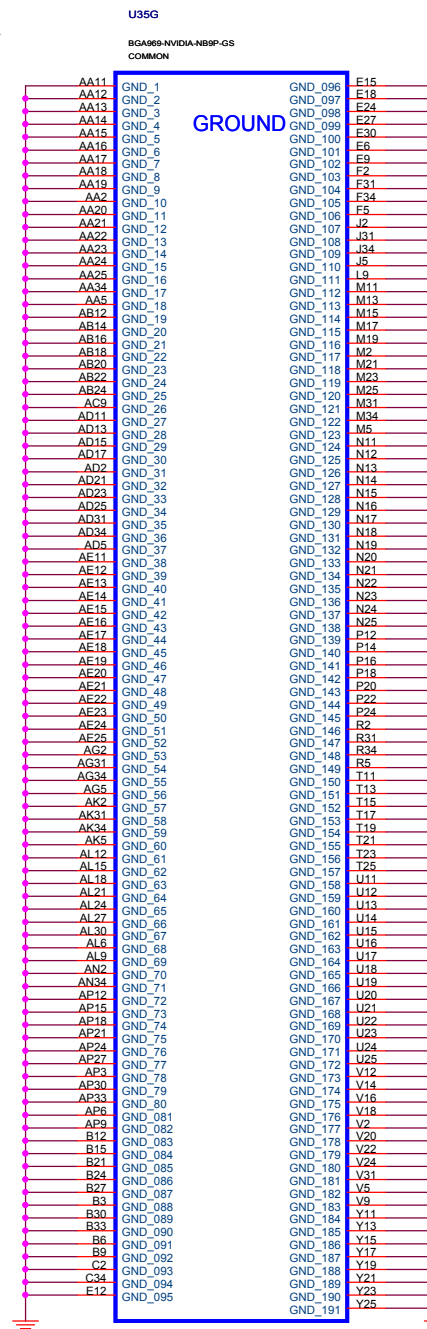
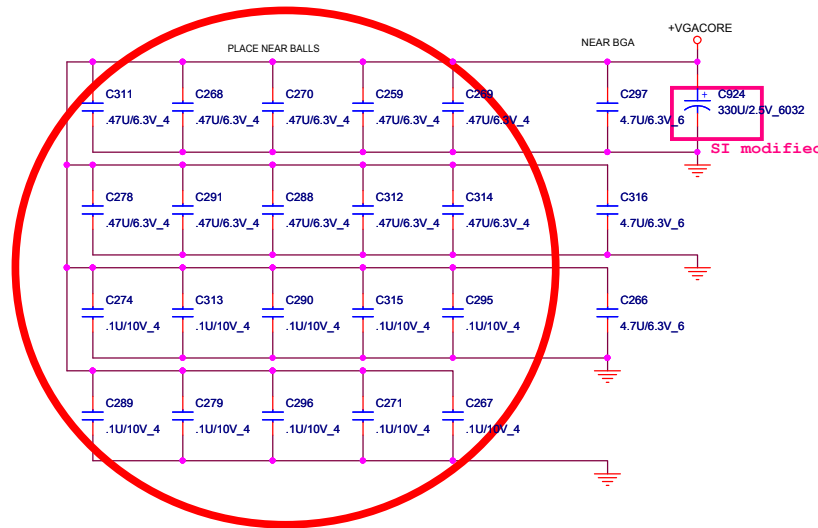
NB9M-GE	0x06E	8	1000 default
NB9M-GS	0x06E	9	1001
NB9P-GE2	0x064	8	1000
NB9P-GS	0x064	9	1001 default

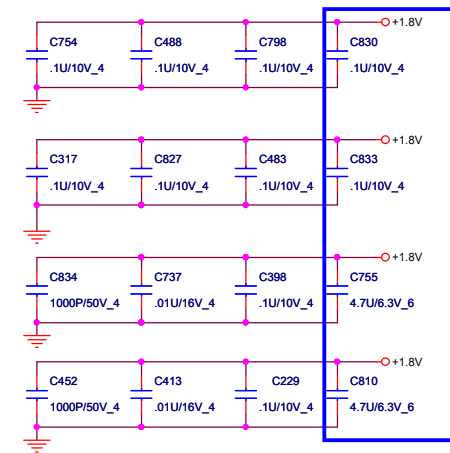
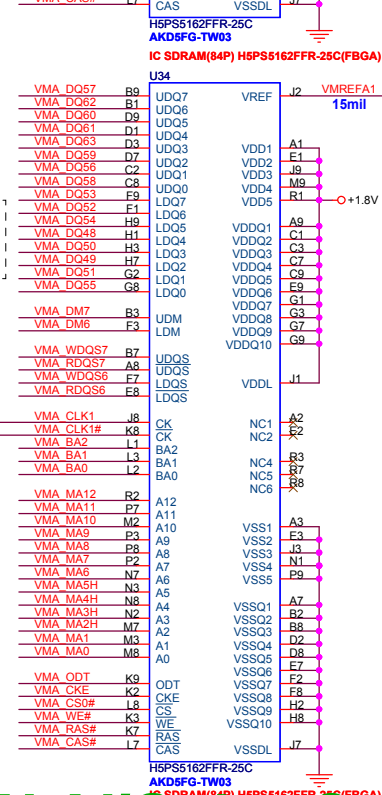
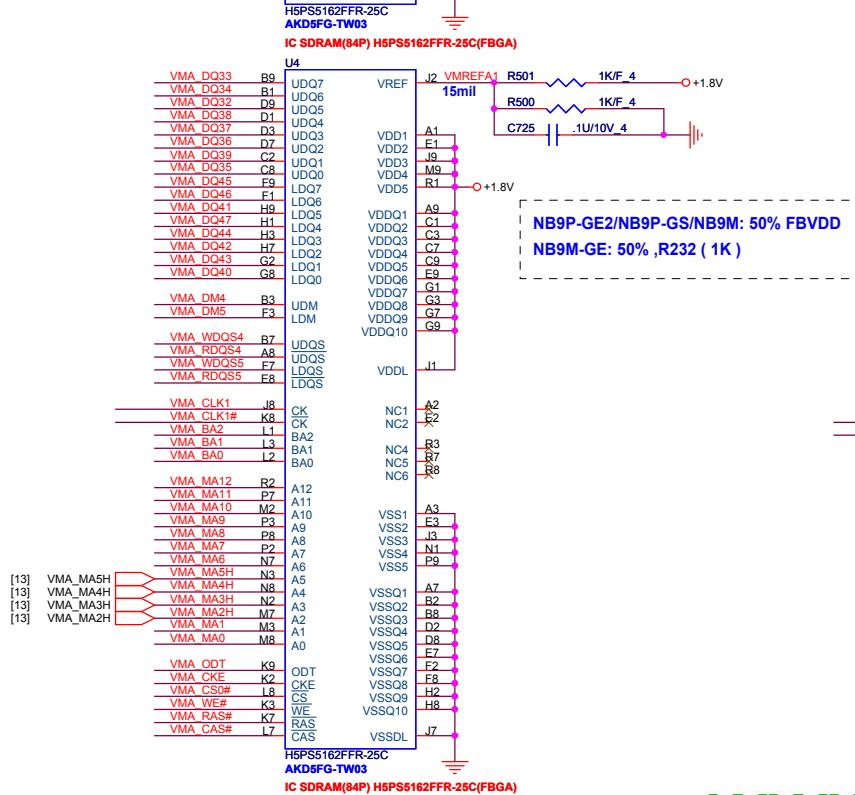
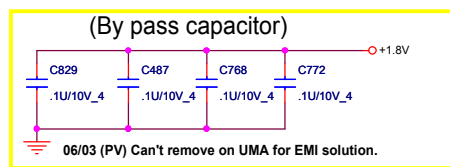
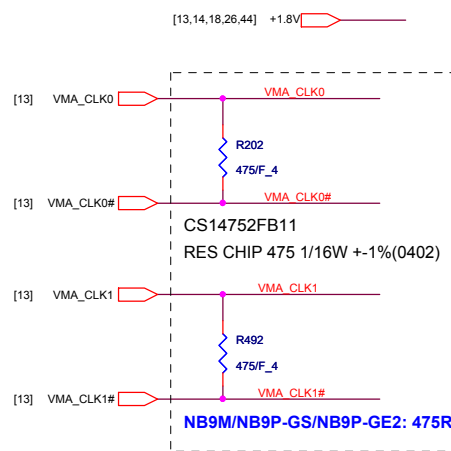
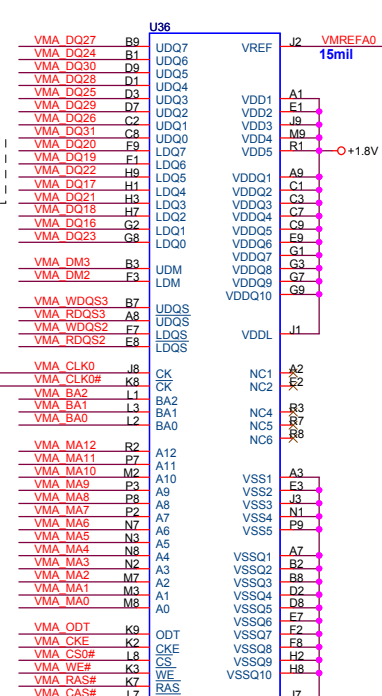
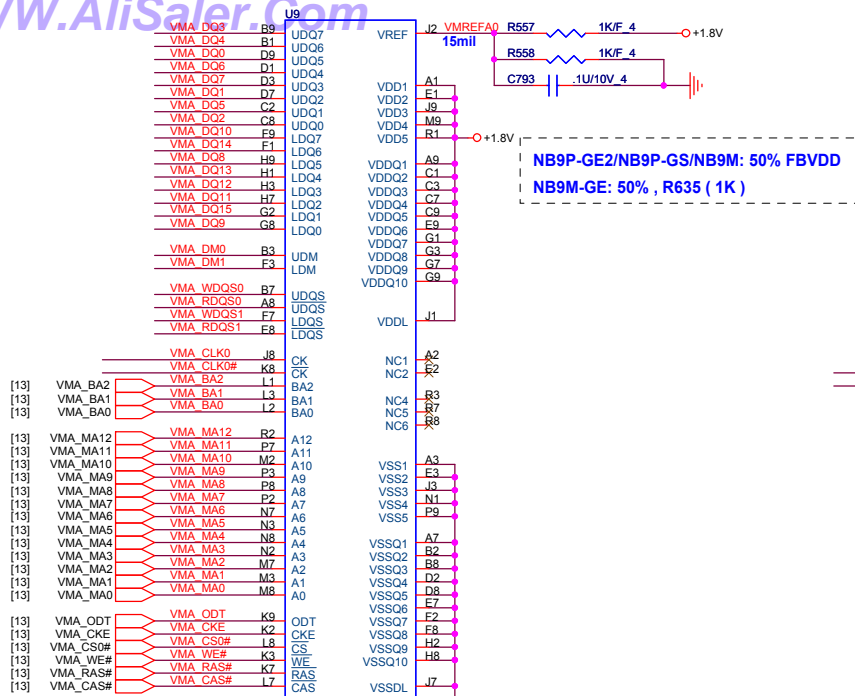
CS33572FB13 RES CHIP 35.7K 1/16W +-1% (0402)

power up sequence



GROUND

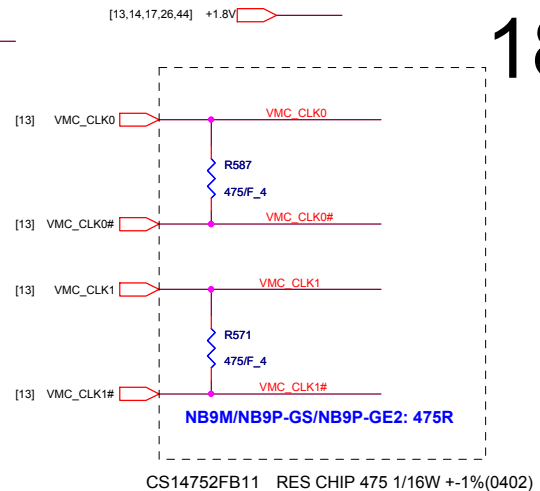
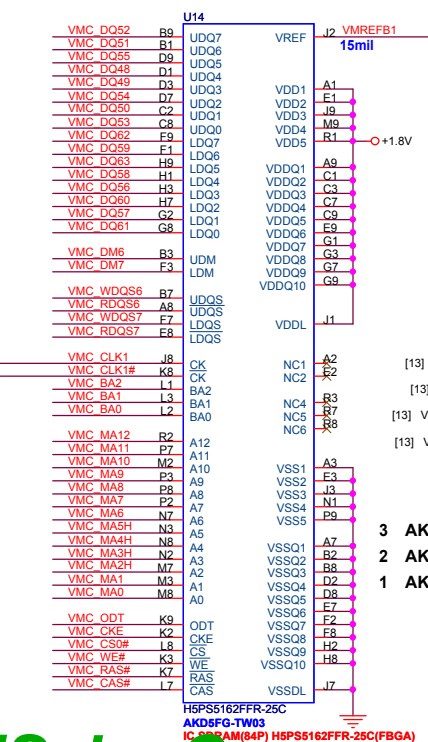
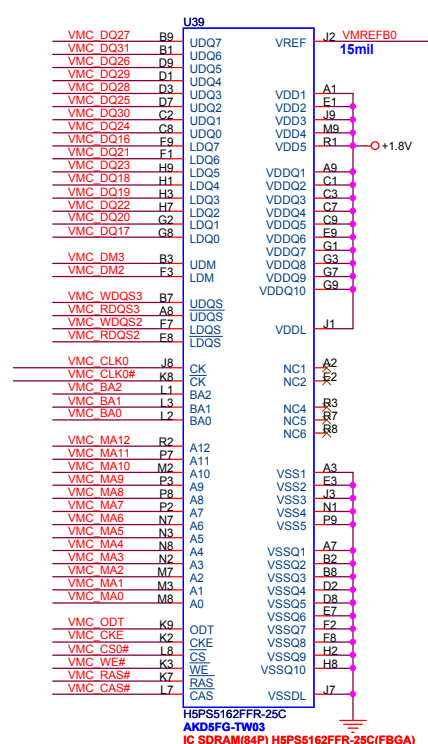
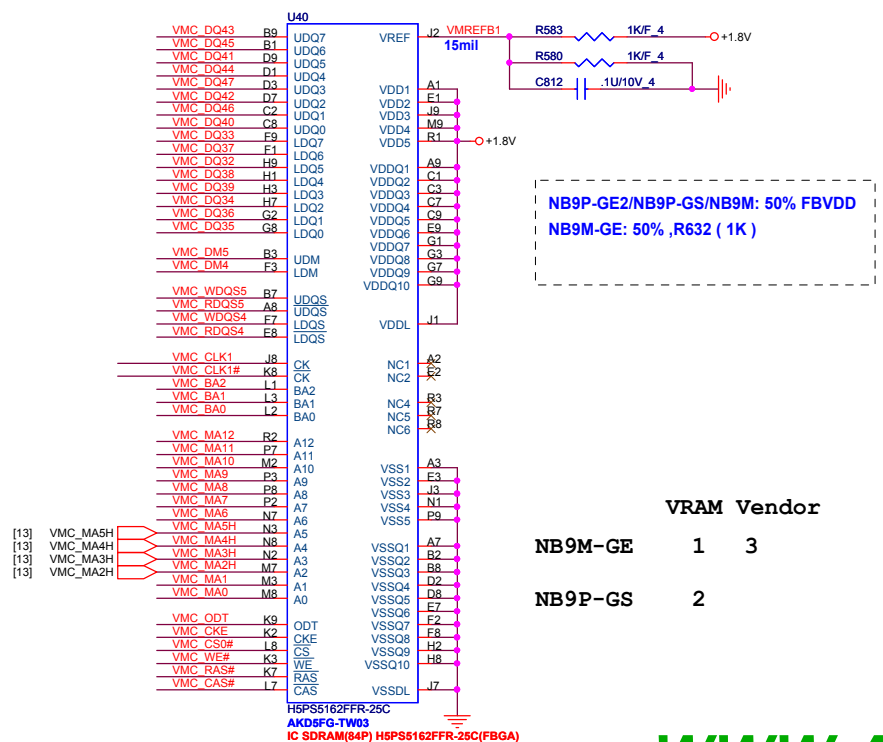
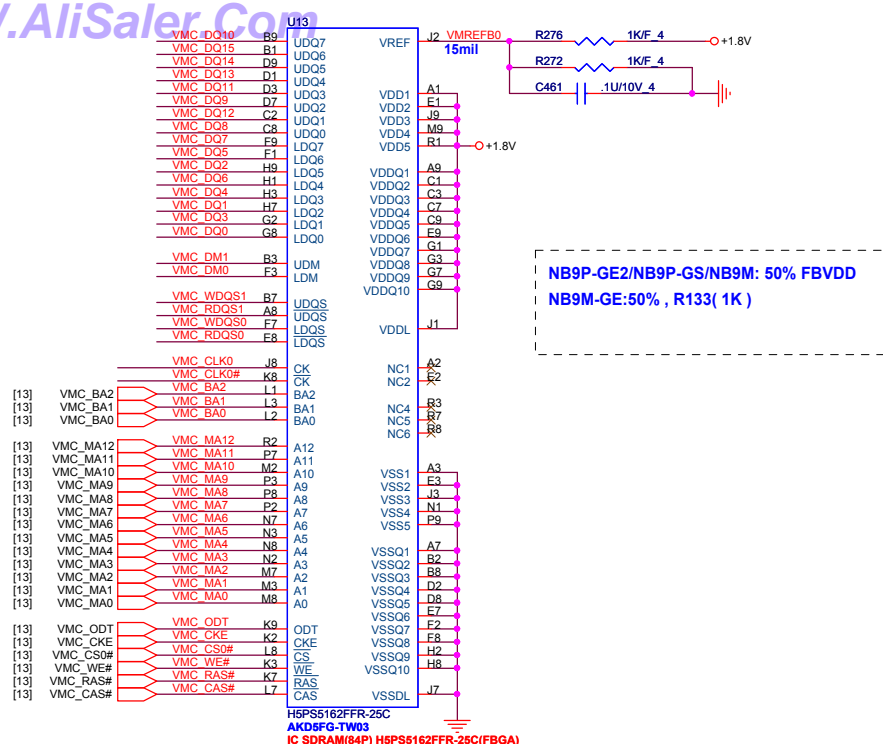




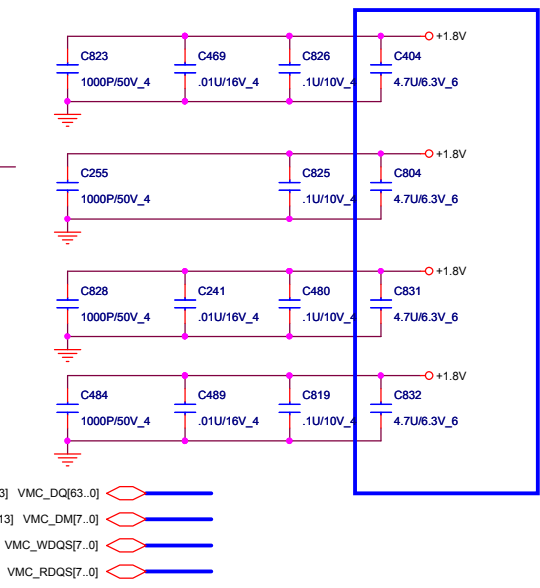
For DB:
 NB9P : AKD59G-T502(Samsung,32M*16)
 NB9M : AKD5FG-TW31(Hynix,32M*16)
 AKD5FG-T*03(Qimonda 32M*16)

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	NV9X VRAM-1(GDDR2 BGA84)	
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CS14752FB11 RES CHIP 475 1/16W +-1%(0402)



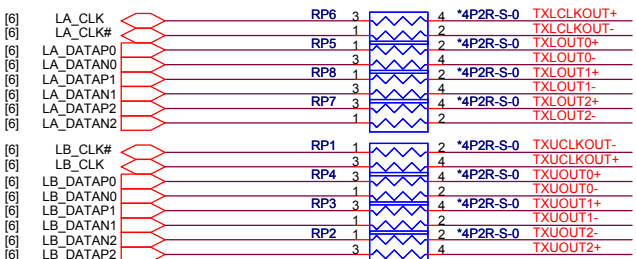
3	AKD5FG-T501	IC SDRAM(84P) K4N51163QG-HC25(FBGA)	Samsung
2	AKD5FG-T^03	IC SDRAM(84P)HYB18T512161B2F-25(TFBGA)	Qimonda
1	AKD5FG-TW31	IC SDRAM(84P) HY5PS121621CFP-25(FBGA)	Hynix



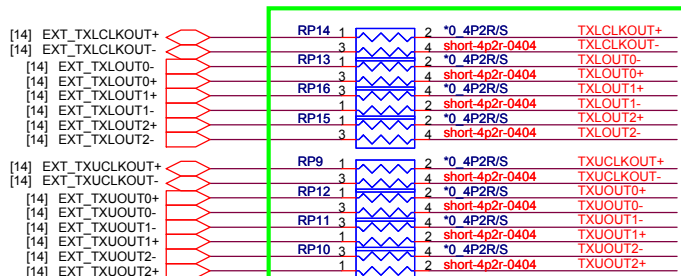
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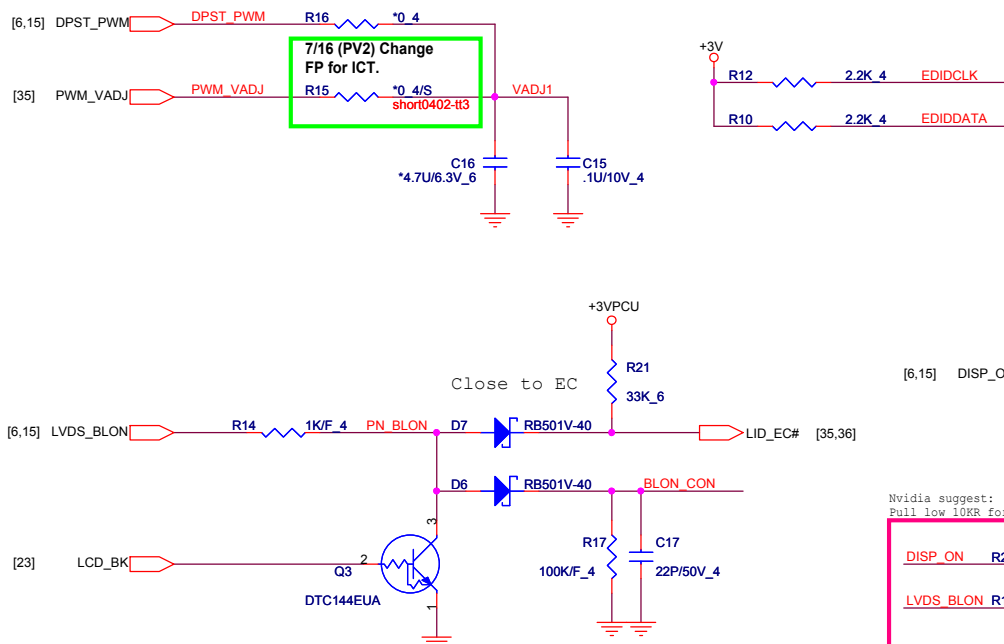
OPTION SIGNAL FROM NB FOR UMA VGA



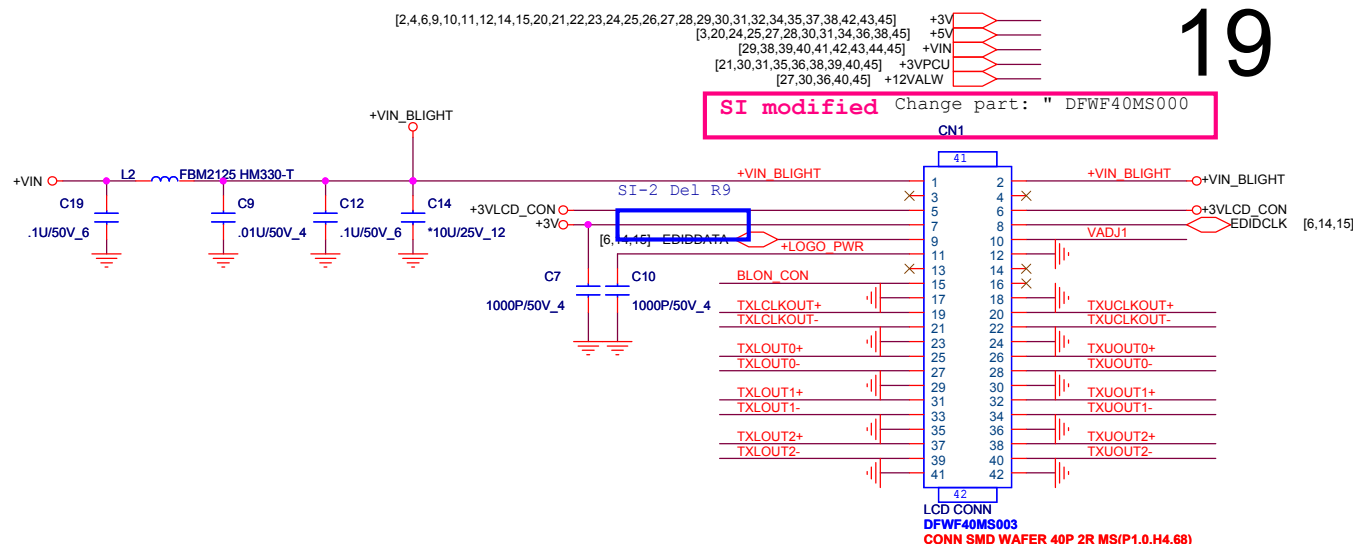
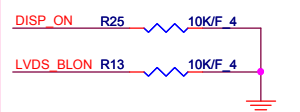
OPTION SIGNAL FROM Nvidia to VGA



07/14 (PV2) Change footprint for PE require.

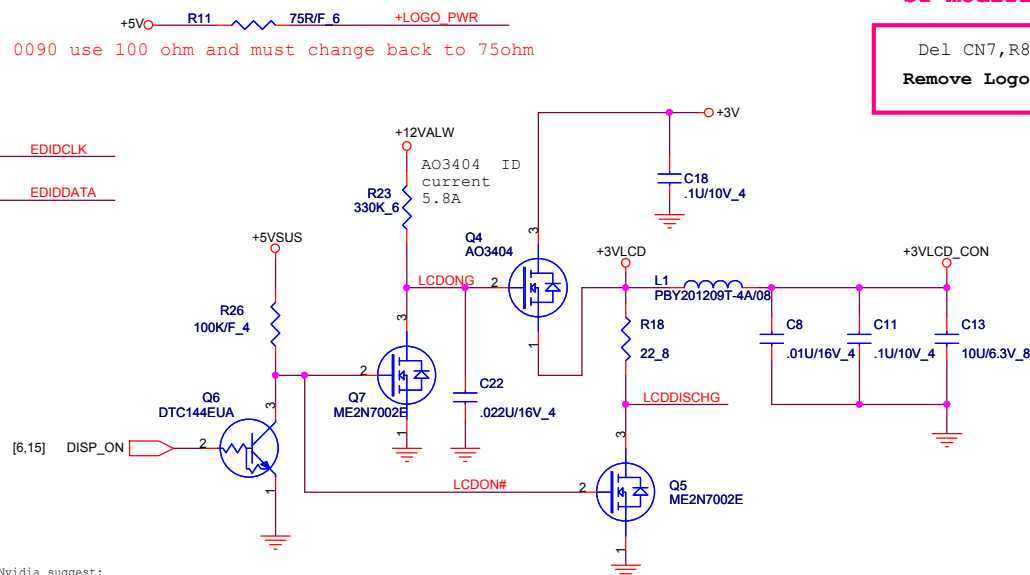


Nvidia suggest:
Pull low 10KR for R95, R7 & R525



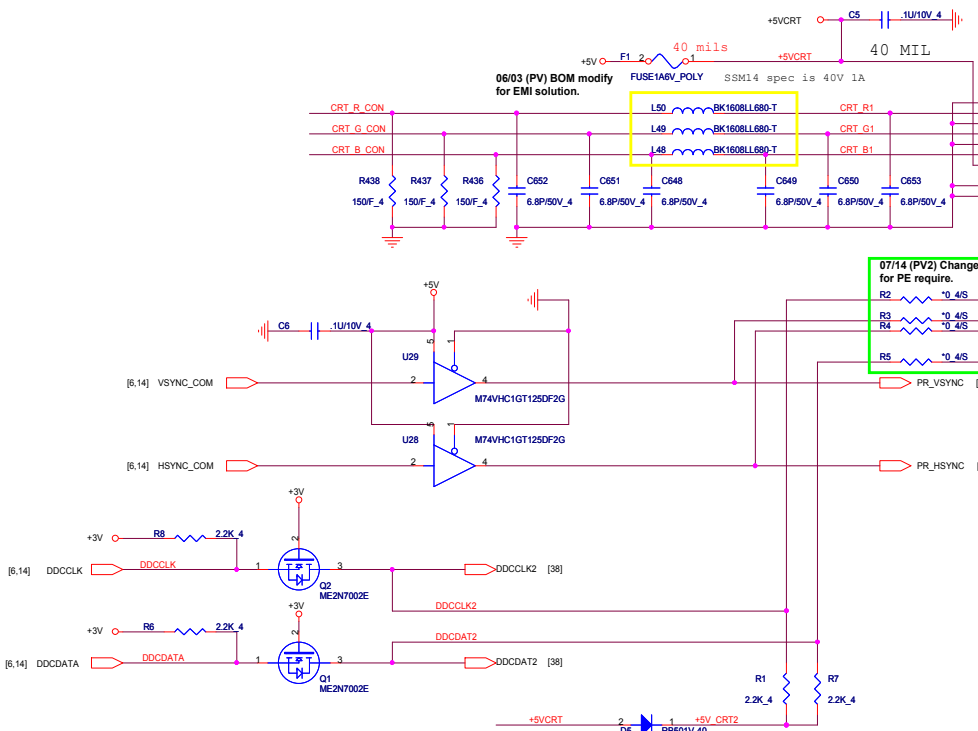
SI modified

Del CN7,R88,C115
Remove Logo light2

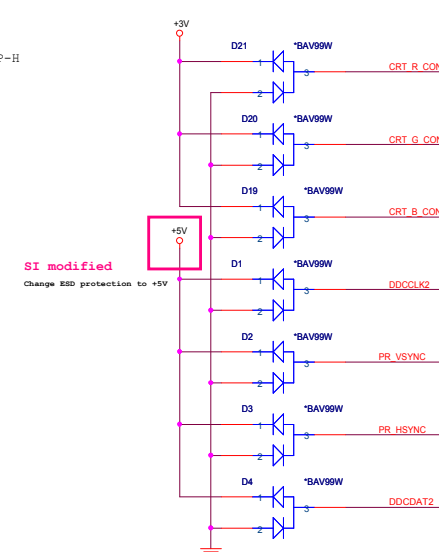
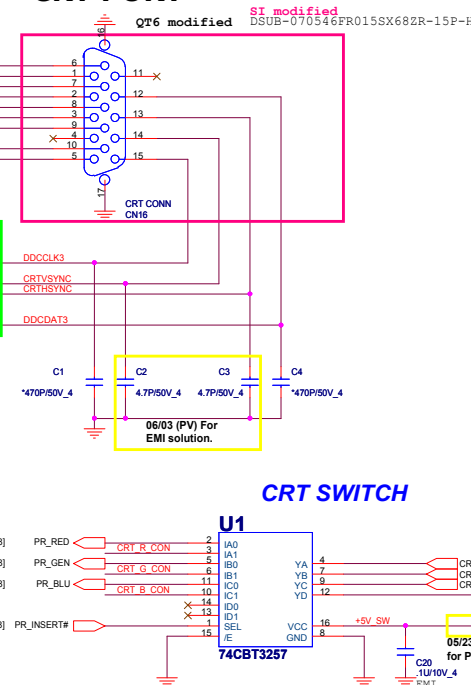


PROJECT : UT6
Quanta Computer Inc.

Size B	Document Number LCD CONN/Lid function	Rev E3
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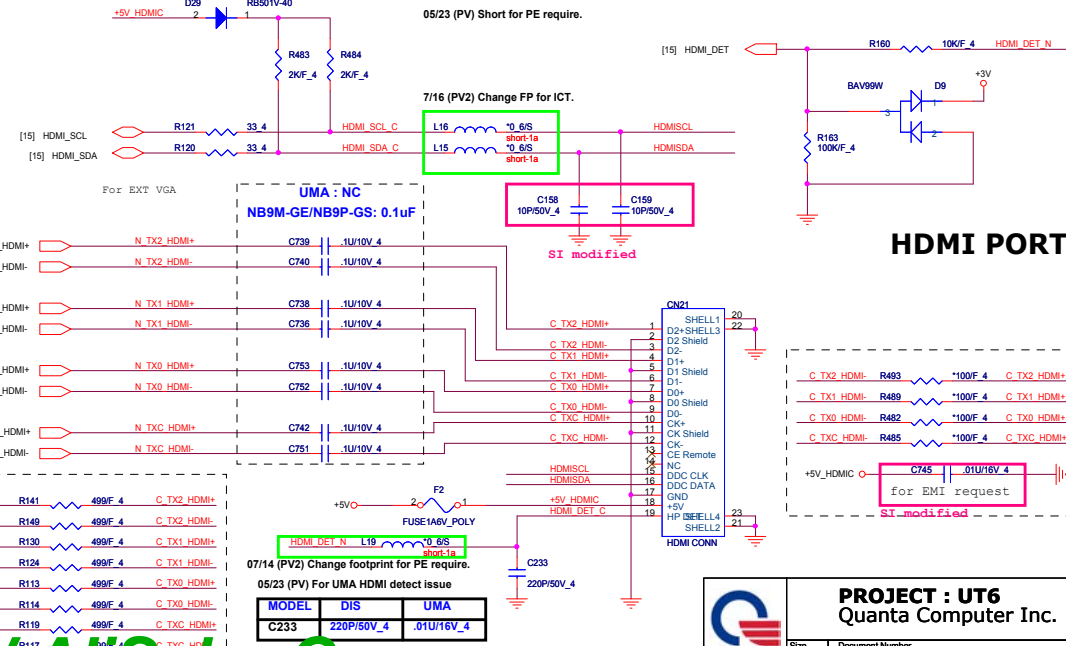
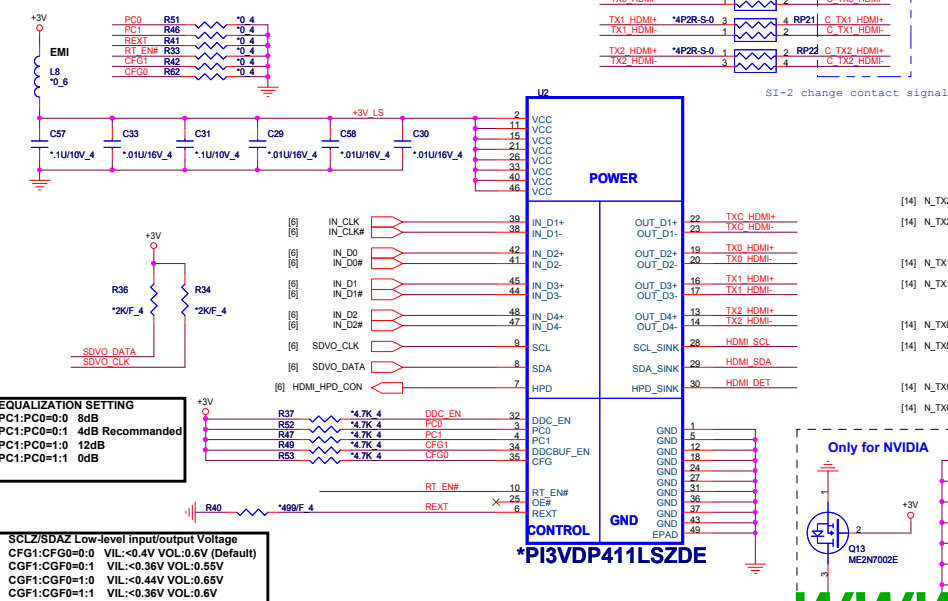


CRT PORT

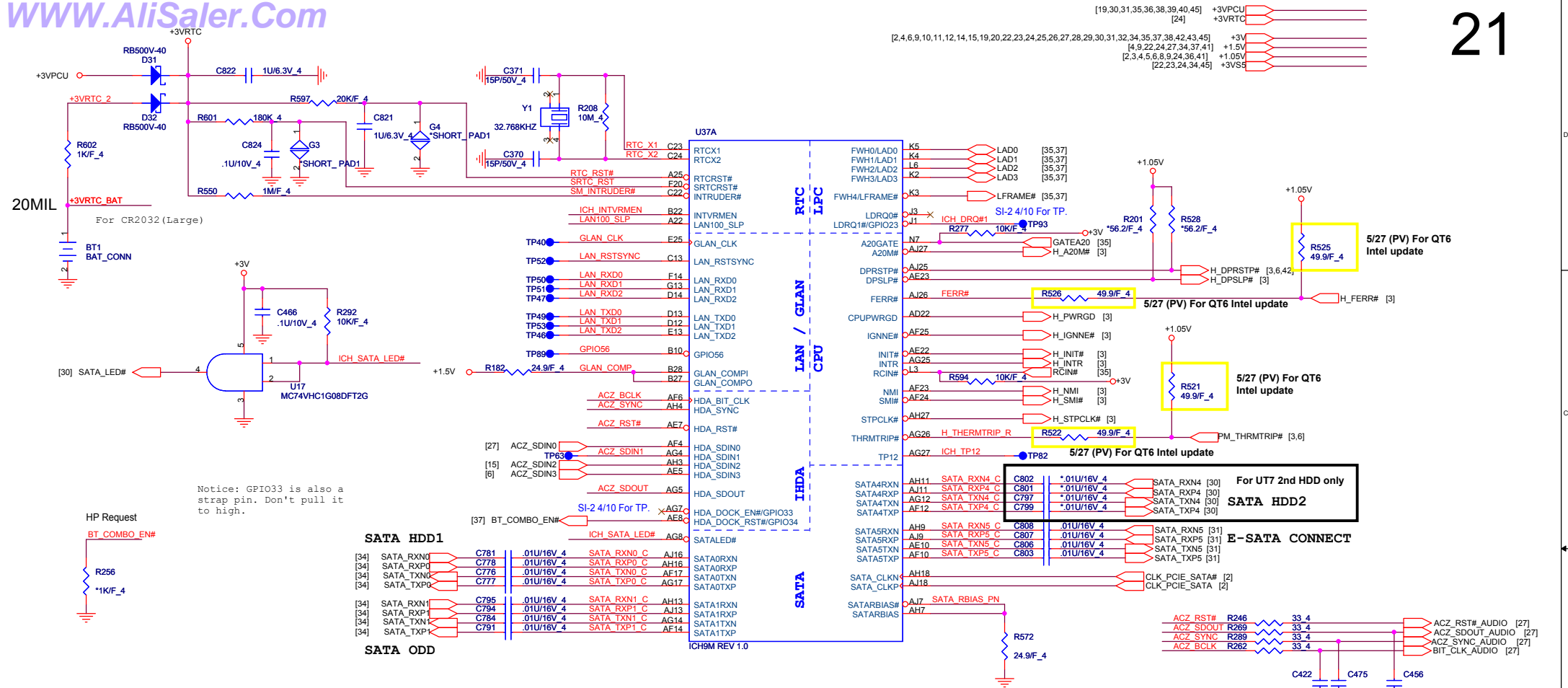


inputs		function
/E	SET	
L	L	Y - port
L	H	Y - port
H	X	Disconnect

For UMA HDMI function



HDMI PORT



SB Strap

ICH9-M Internal VR Enable strap
(Internal VR for VccSus1_05, VccSus1_5 and VccCL1_5)

Low = Internal VR disable
High = Internal VR enable(Default)

ICH9-M LAN100_SLP Strap
(Internal VR for VccLAN1_05 and VccCL1_05)

Low = Internal VR disable
High = Internal VR enable(Default)

ICH9-M SATA HDD1

[34] SATA_RXN0	C781	.01U/16V_4	SATA_RXN0_C	AJ18
[34] SATA_RXP0	C778	.01U/16V_4	SATA_RXP0_C	AH18
[34] SATA_TXN0	C776	.01U/16V_4	SATA_TXN0_C	AE17
[34] SATA_TXP0	C777	.01U/16V_4	SATA_TXP0_C	AG17

SATA HDD2

[34] SATA_RXN1	C795	.01U/16V_4	SATA_RXN1_C	AH13
[34] SATA_RXP1	C794	.01U/16V_4	SATA_RXP1_C	AJ13
[34] SATA_TXN1	C784	.01U/16V_4	SATA_TXN1_C	AG14
[34] SATA_TXP1	C791	.01U/16V_4	SATA_TXP1_C	AF14

SATA ODD

[34] SATA_RXN2	C781	.01U/16V_4	SATA_RXN2_C	AJ18
[34] SATA_RXP2	C778	.01U/16V_4	SATA_RXP2_C	AH18
[34] SATA_TXN2	C776	.01U/16V_4	SATA_TXN2_C	AE17
[34] SATA_TXP2	C777	.01U/16V_4	SATA_TXP2_C	AG17

XOR Chain Entrance Strap

ICH_TP3	HDA_SDOUT	Description
0	0	RSVD
0	1	Enter XOR Chain
1	0	Normal operation(Default)
1	1	Set PCIE port config bit 1

ICH9 Boot BIOS select

STRAP	PCI_GNT0#	SPI_CS#1
SPI	0	1
PCI	1	0
LPC	1	1

(default)

A16 swap override strap

PCI_GNT#3	Low = A16 swap override enabled Hi = Default
-----------	---

No Reboot Strap

ACZ_SPKR	Low: Default Hi: No reboot
----------	-------------------------------

TPM physical presence

ICH_GPIO57	Low: Default
------------	--------------

For GM UMA only

ACZ_RST#	R242	*33_4	ACZ_RST#_MCH	[6]
ACZ_SDOUT	R266	*33_4	ACZ_SDOUT_MCH	[6]
ACZ_SYNC	R274	*33_4	ACZ_SYNC_MCH	[6]
ACZ_BCLK	R258	*33_4	ACZ_BITCLK_MCH	[6]

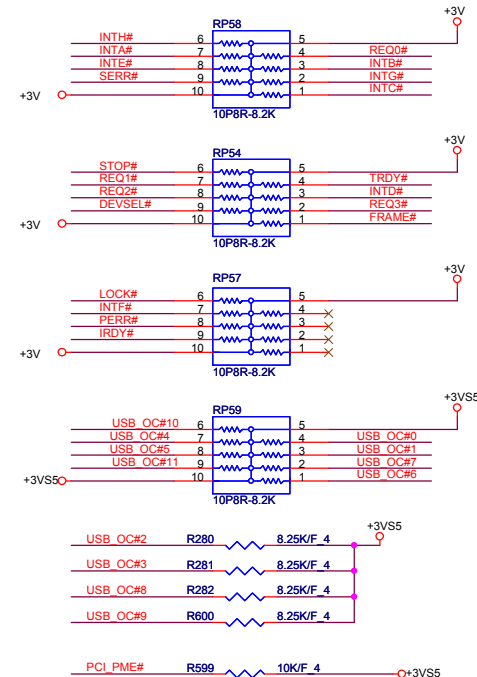
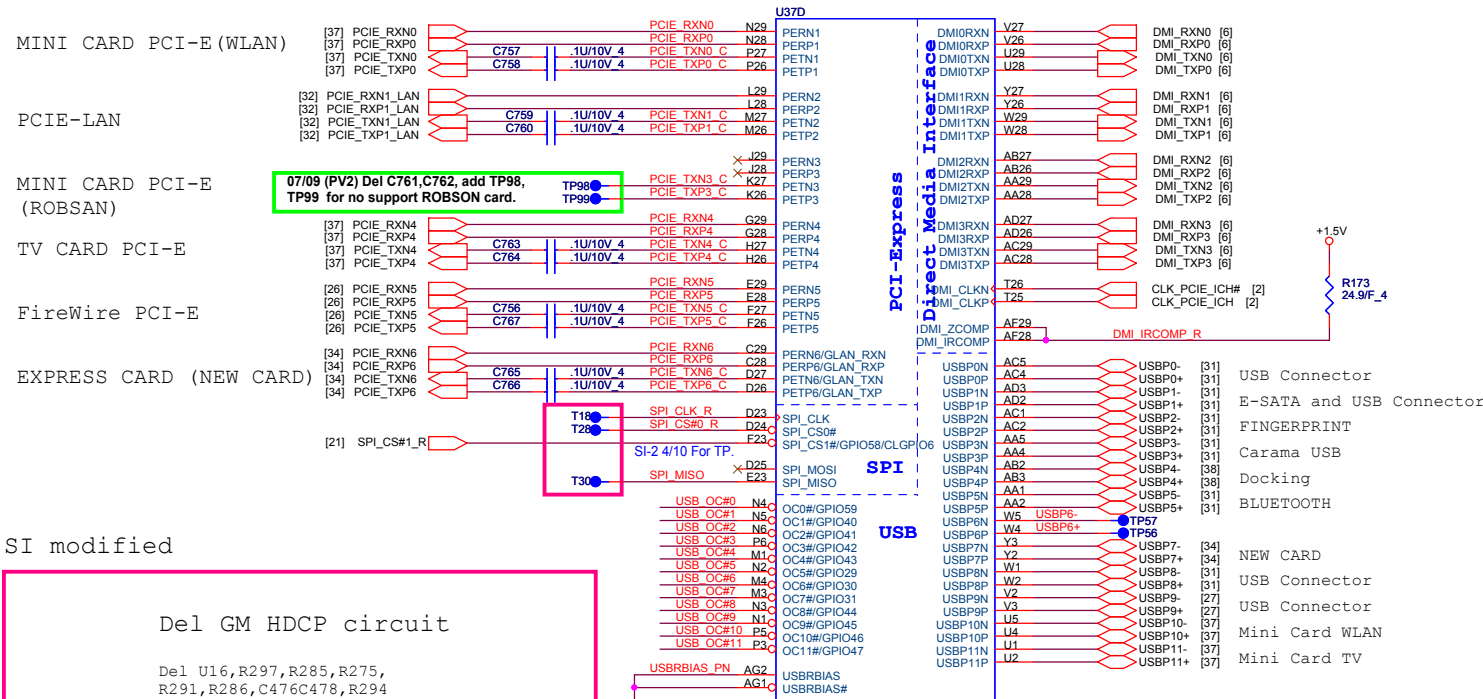
Close to U45

ACZ_RST#	R252	22_4	NV_HDA_RST	[15]
ACZ_SDOUT	R268	22_4	NV_HDA_SDO	[15]
ACZ_SYNC	R283	22_4	NV_HDA_SYNC	[15]
ACZ_BCLK	R261	22_4	NV_HDA_BCLK	[15]

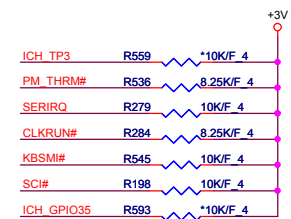
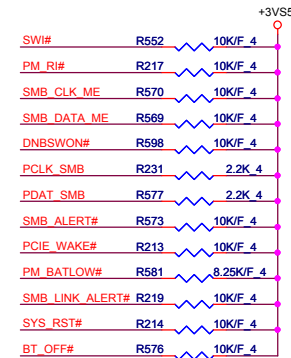
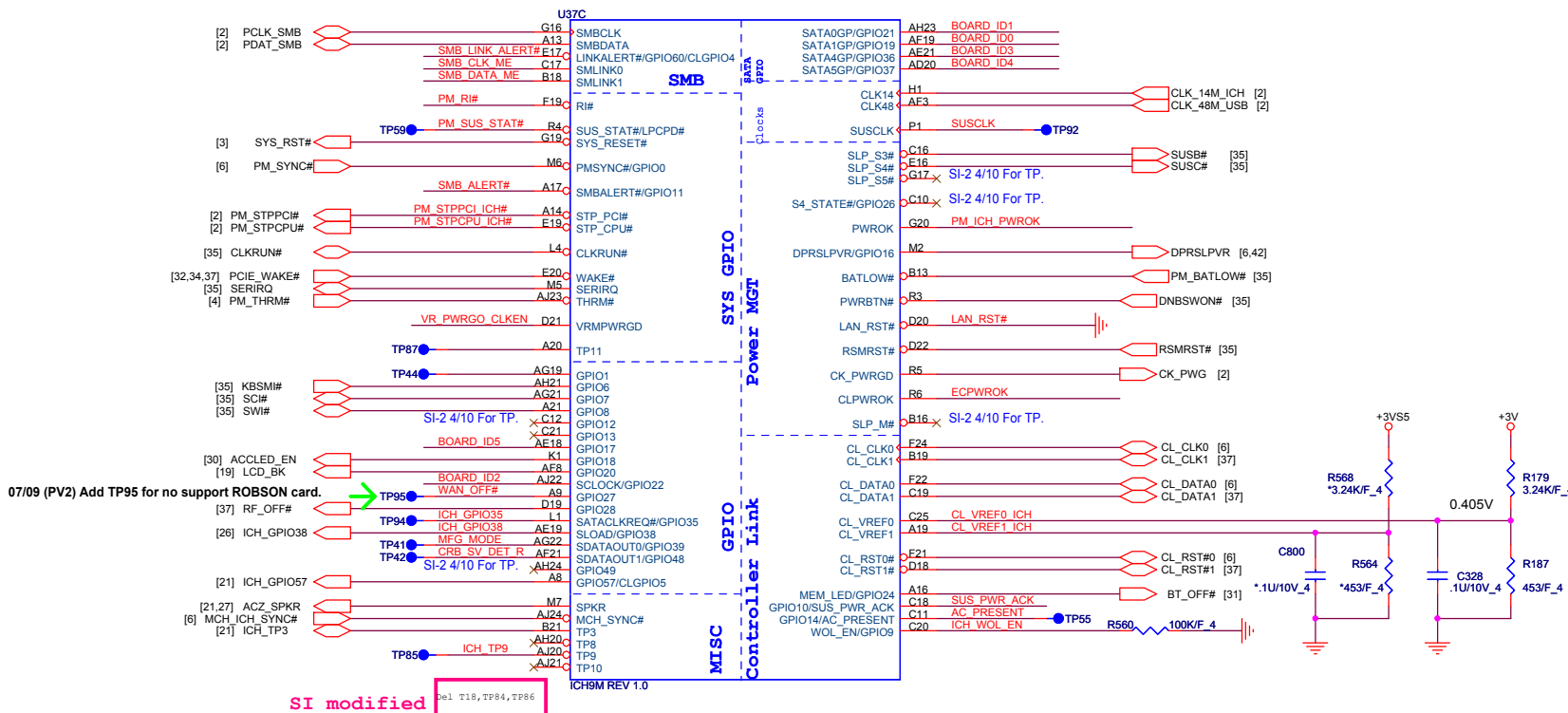
PROJECT : UT6
Quanta Computer Inc.

Size Custom Document Number ICH9-M Host 1/4 Rev E3A

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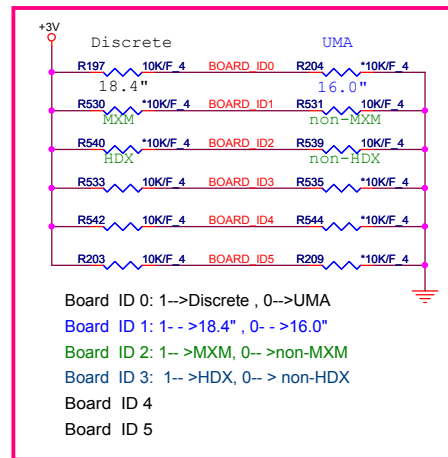
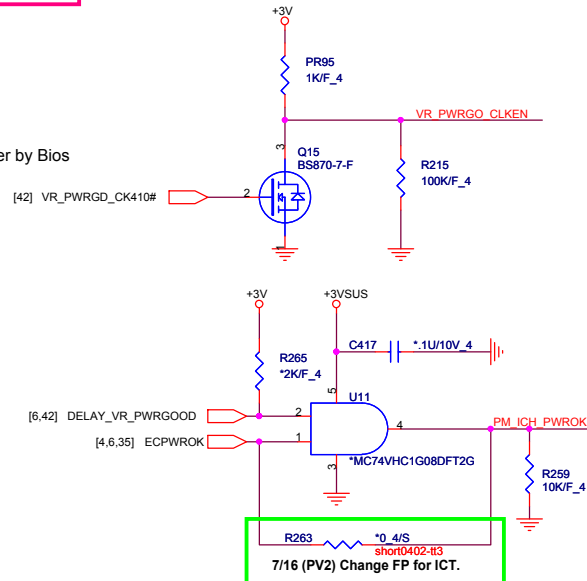


[2,4,6,9,10,11,12,14,15,19,20,21,22,24,25,26,27,28,29,30,31,32,34,35,37,38,42,43,45] +3V
[21,22,24,34,45] +3VS5
[31,37,41,42,43,45] +3VSUS



SI-2 Build

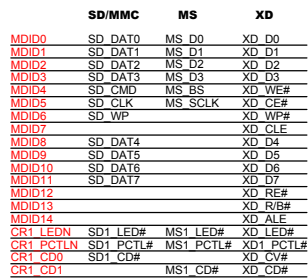
Delete R574,G2 as Bios_Rec can be cover by Bios



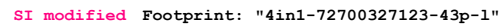
Board ID 0: 1-->Discrete, 0-->UMA
Board ID 1: 1-->18.4", 0-->16.0"
Board ID 2: 1-->MXM, 0-->non-MXM
Board ID 3: 1-->HDX, 0-->non-HDX
Board ID 4
Board ID 5



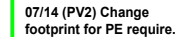
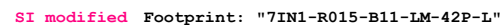
25

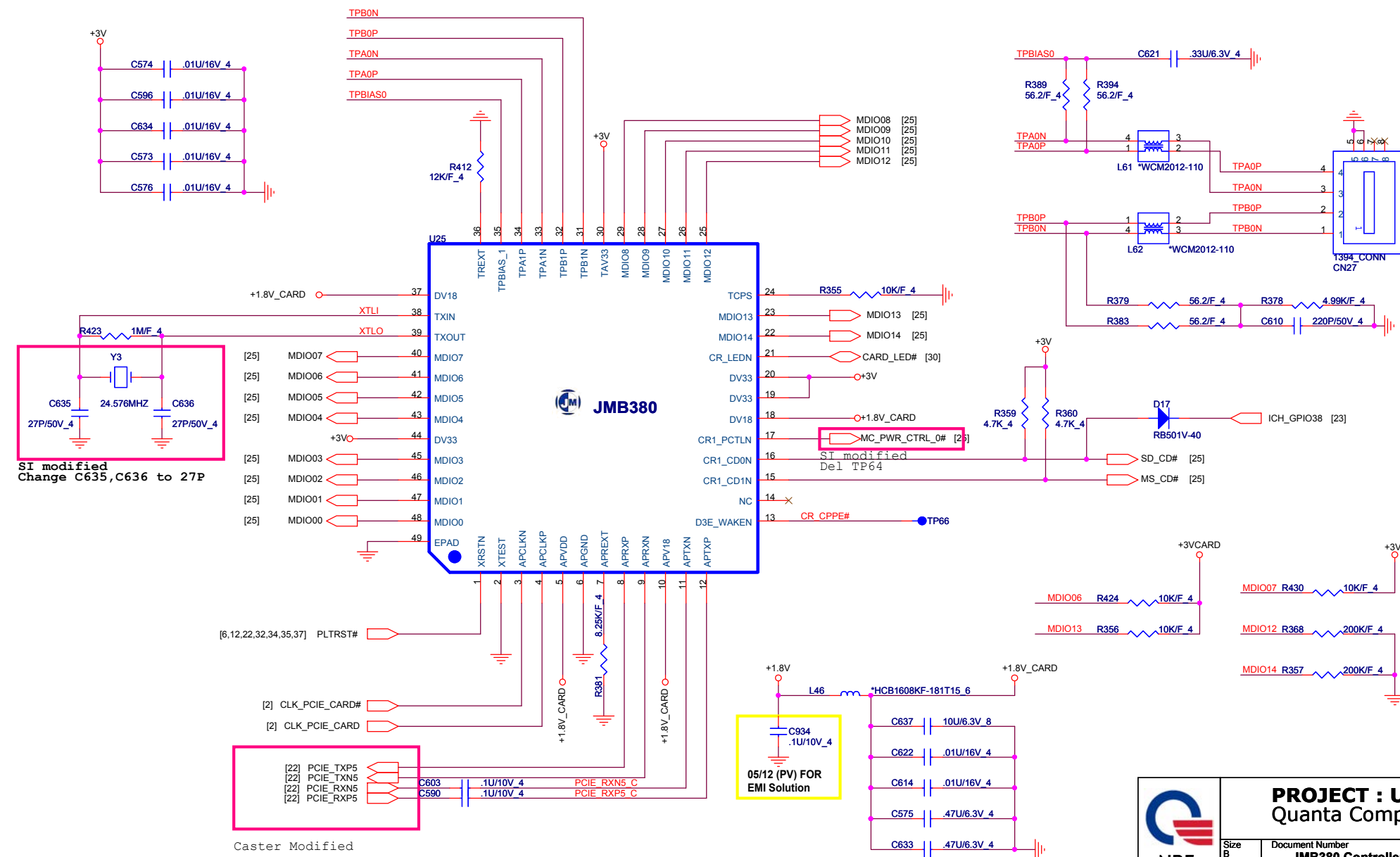


XD,MMC/SD,MS/MSP




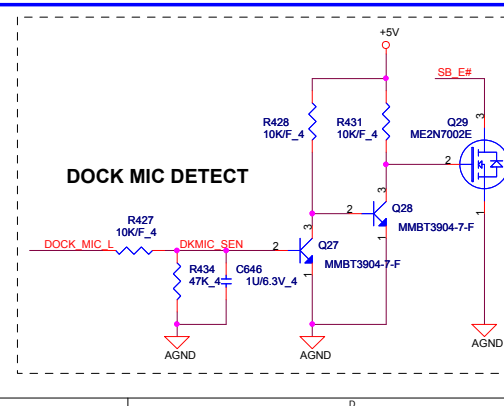
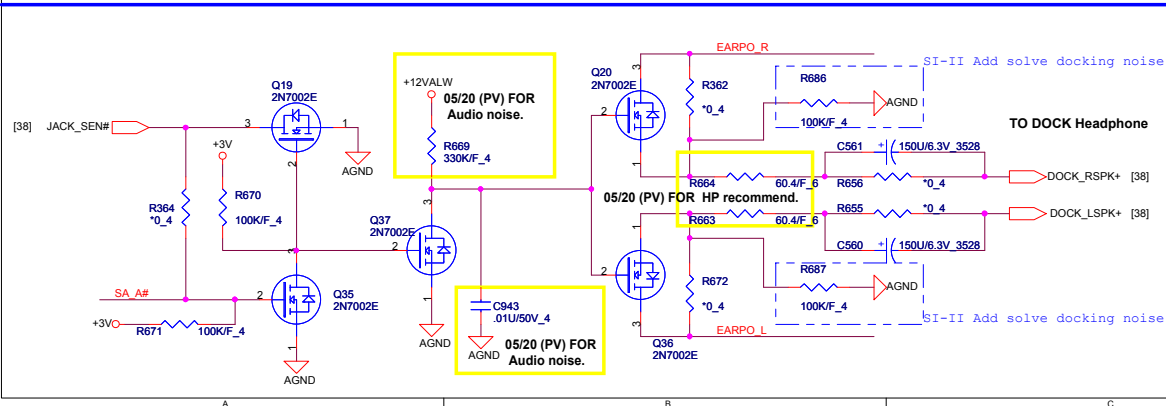
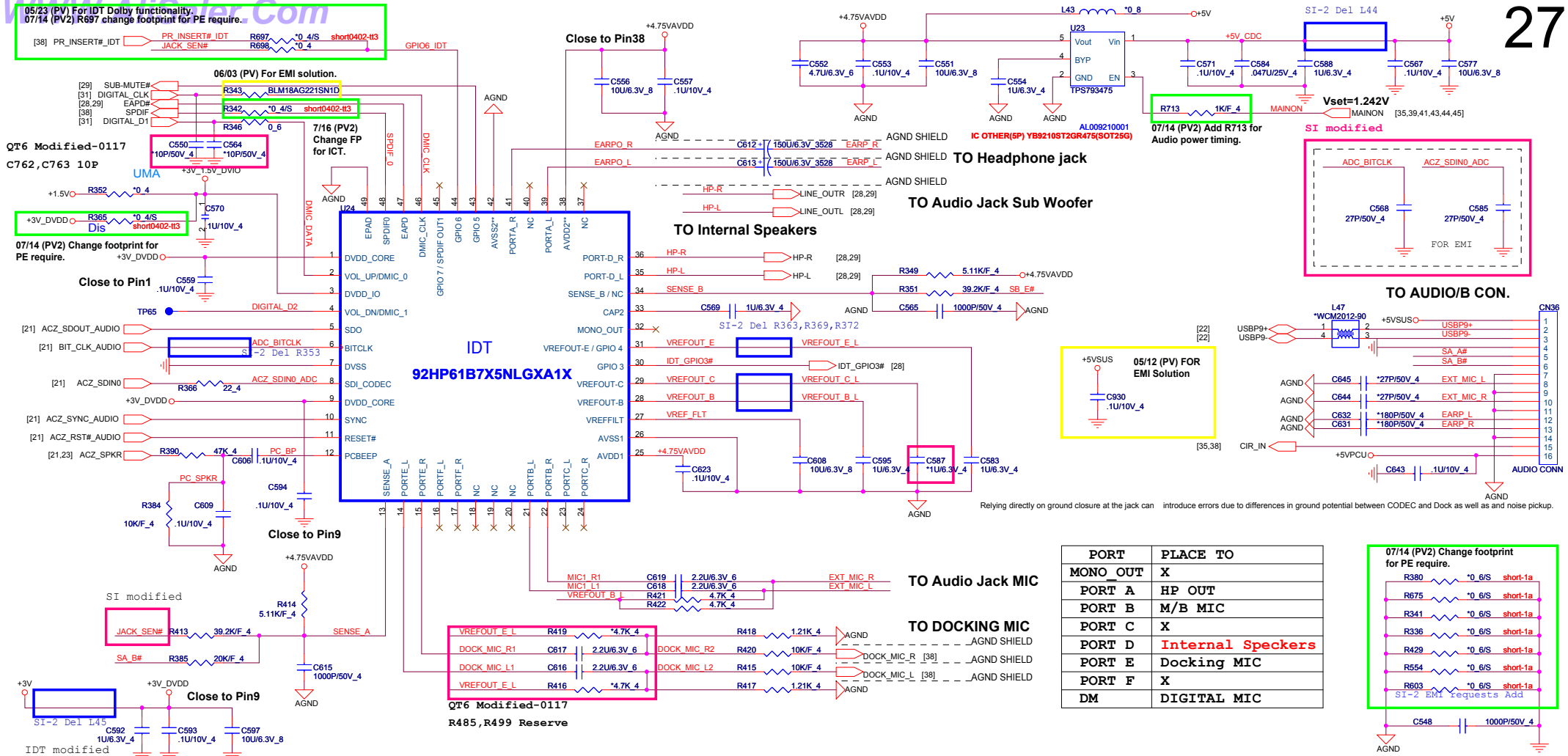
2ND SOURCE





05/23 (PV) For IDT Dolby functionality.
07/14 (PV2) R697 change footprint for PE require.

[38] PR_INSERT#_IDT  PR_INSERT#_IDT R697 *0 4/S short0402-tt3
JACK_SEN# R698 *0 4

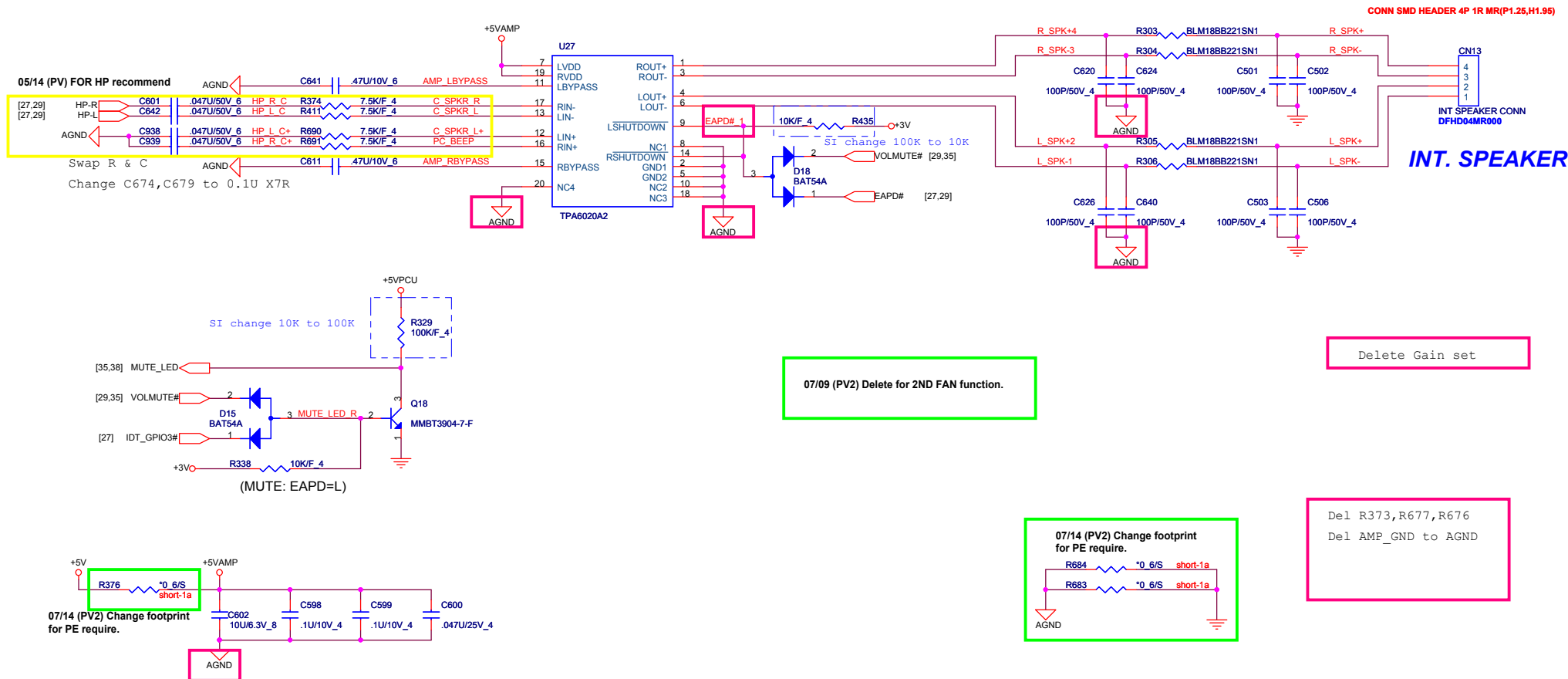


```
SA_A# -->EXT HP
SA_B# -->EXT MIC
SB_E#--> DOCK MIC
Audio JACK: Normal Open
```

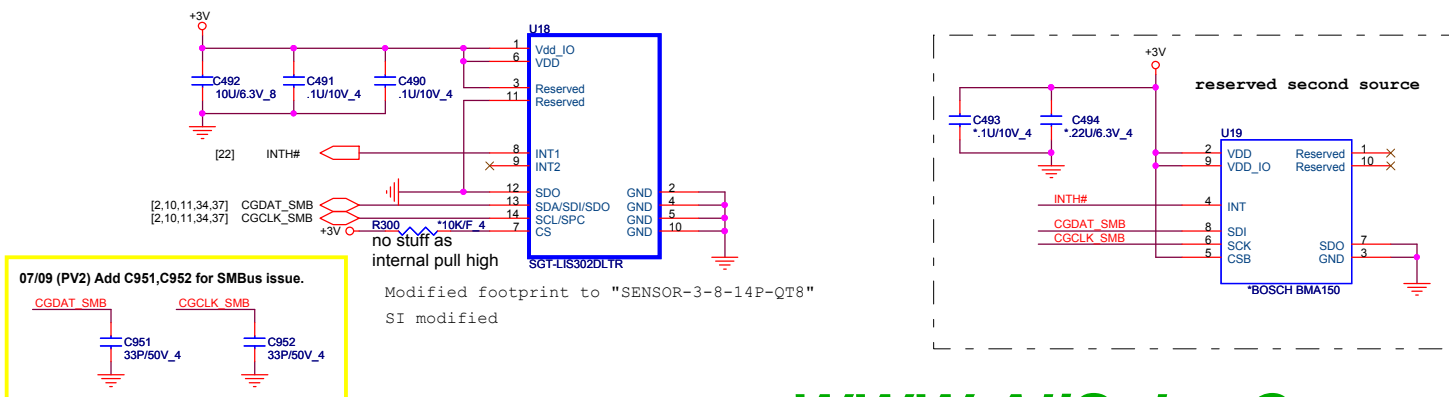


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Quanta Computer Inc.

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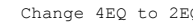


Accelerometer Sensor

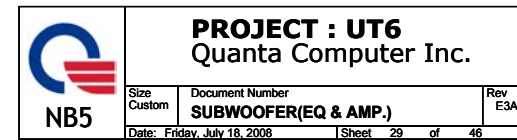


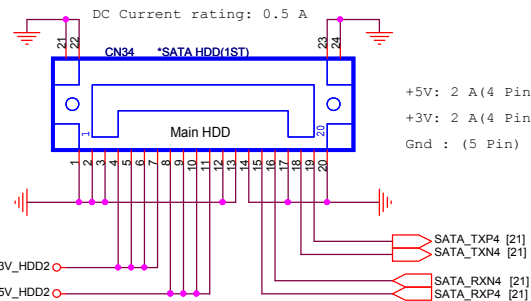
PROJECT : UT6
Quanta Computer Inc.

Size Custom	Document Number AMP_TPA6017/Accelerometer	Rev E3A
Date: Friday, July 18, 2008	Sheet 28	of 46

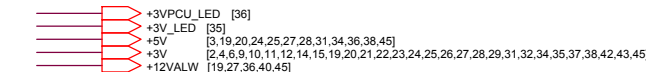
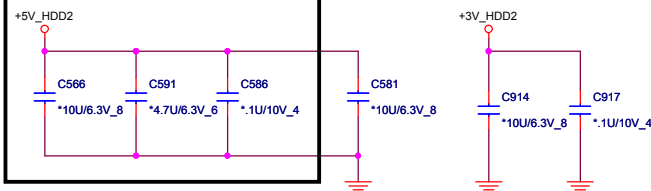


Sub-Woofer power

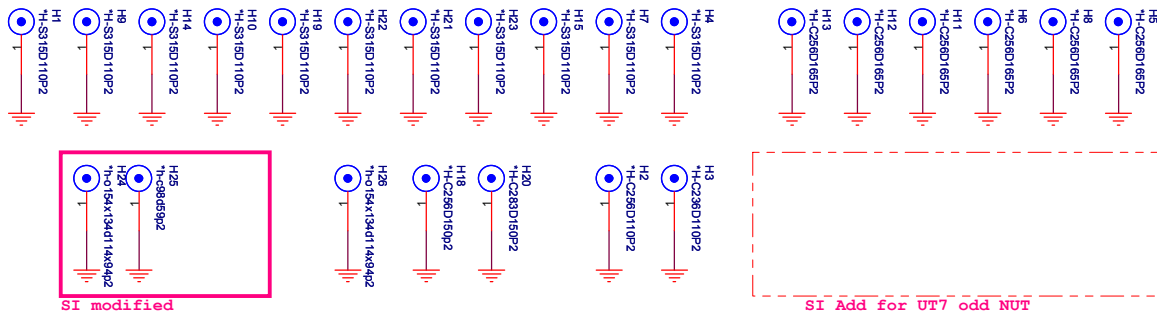




FOR UT7 2ND HDD ONLY.

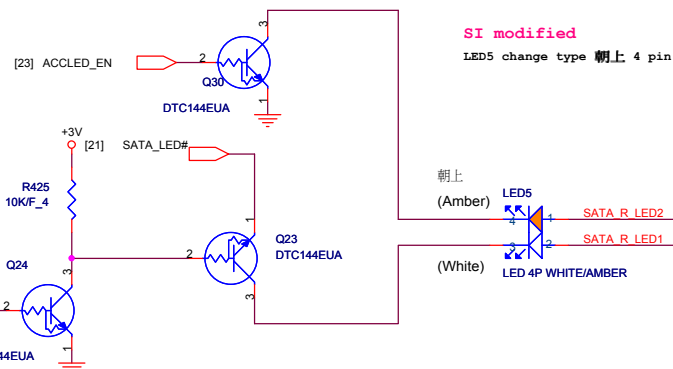
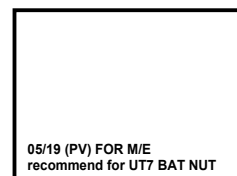
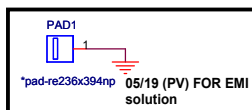


M/B Screw Hole

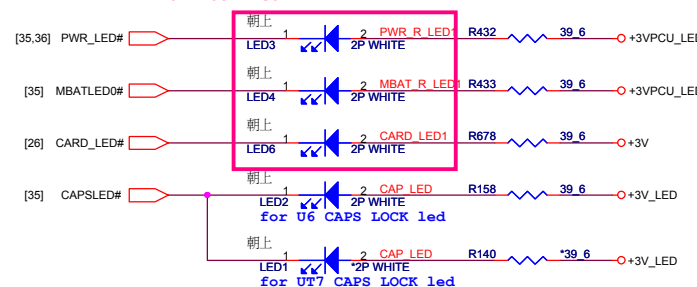


delete all PAD & change screw footprint

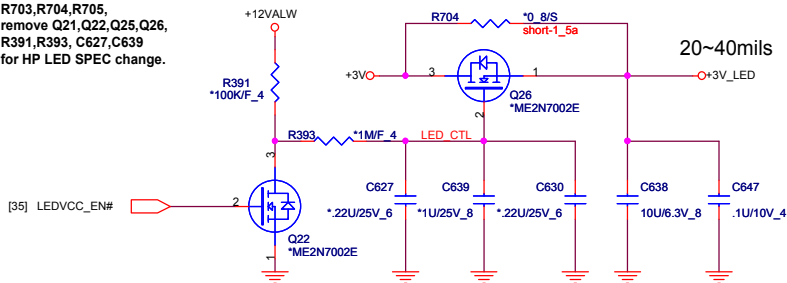
07/14 (PV2) Delete H16,H17 for no support ROBSON card.



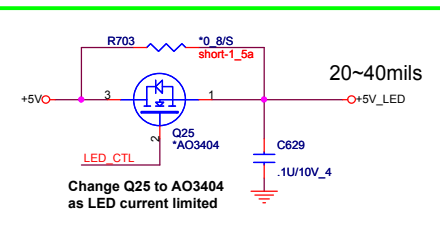
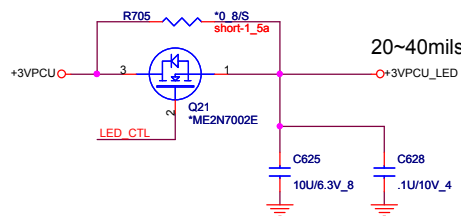
SI modified LED3,4,6 change type 朝上 2 pin



07/09 (PV2) Add R703,R704,R705, remove Q21,Q22,Q25,Q26, R391,R393, C627,C639 for HP LED SPEC change.

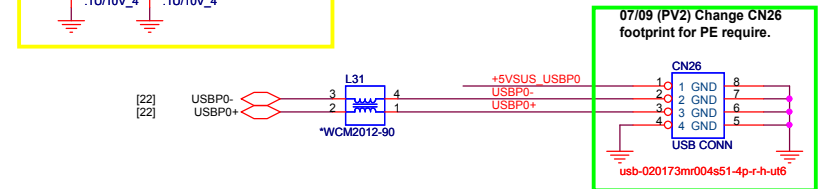
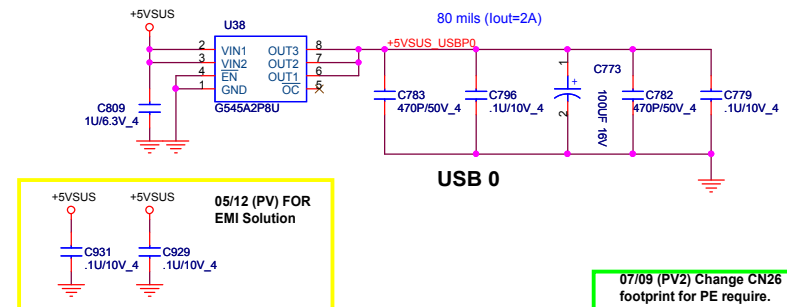
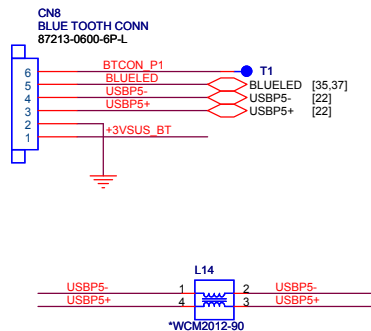
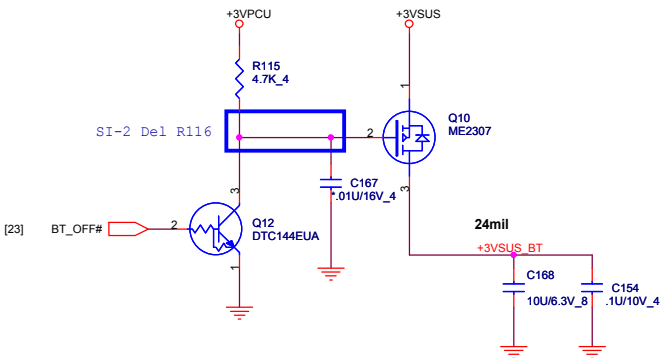


LED PWR CONTROL

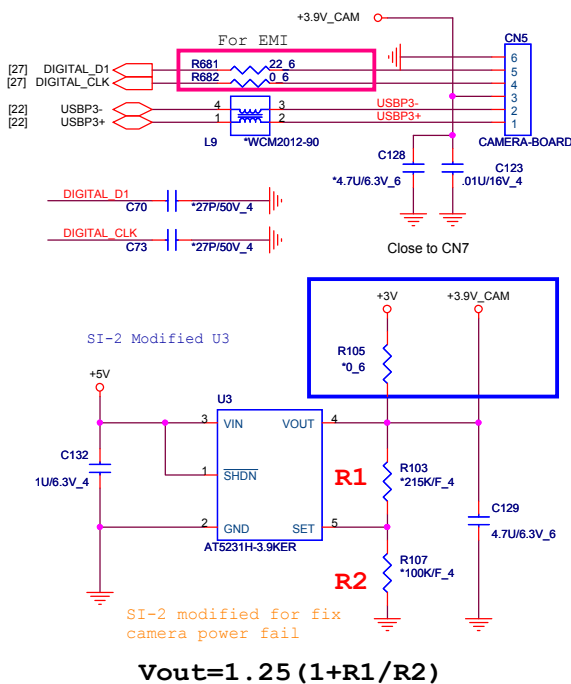


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Quanta Computer Inc.

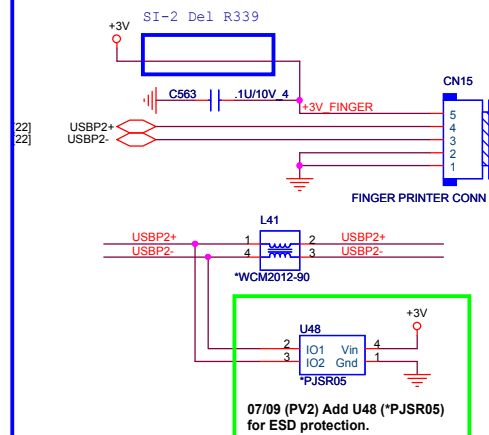
Size	Document Number	Rev
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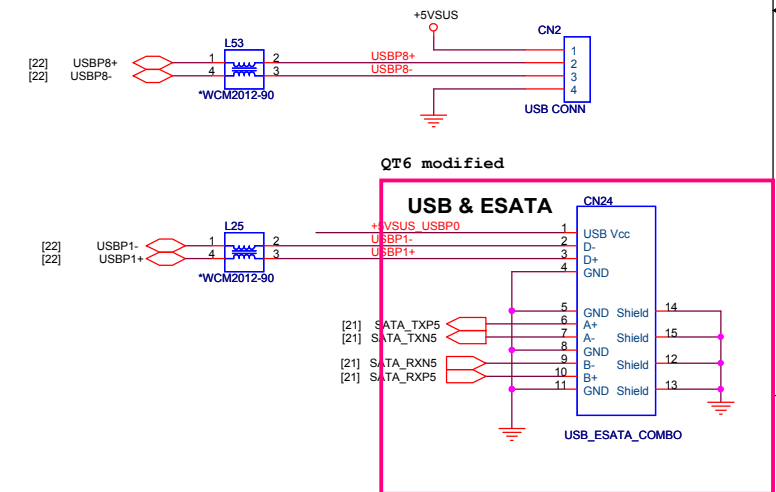
USB CAMERA /DIGITAL MIC CONNECT

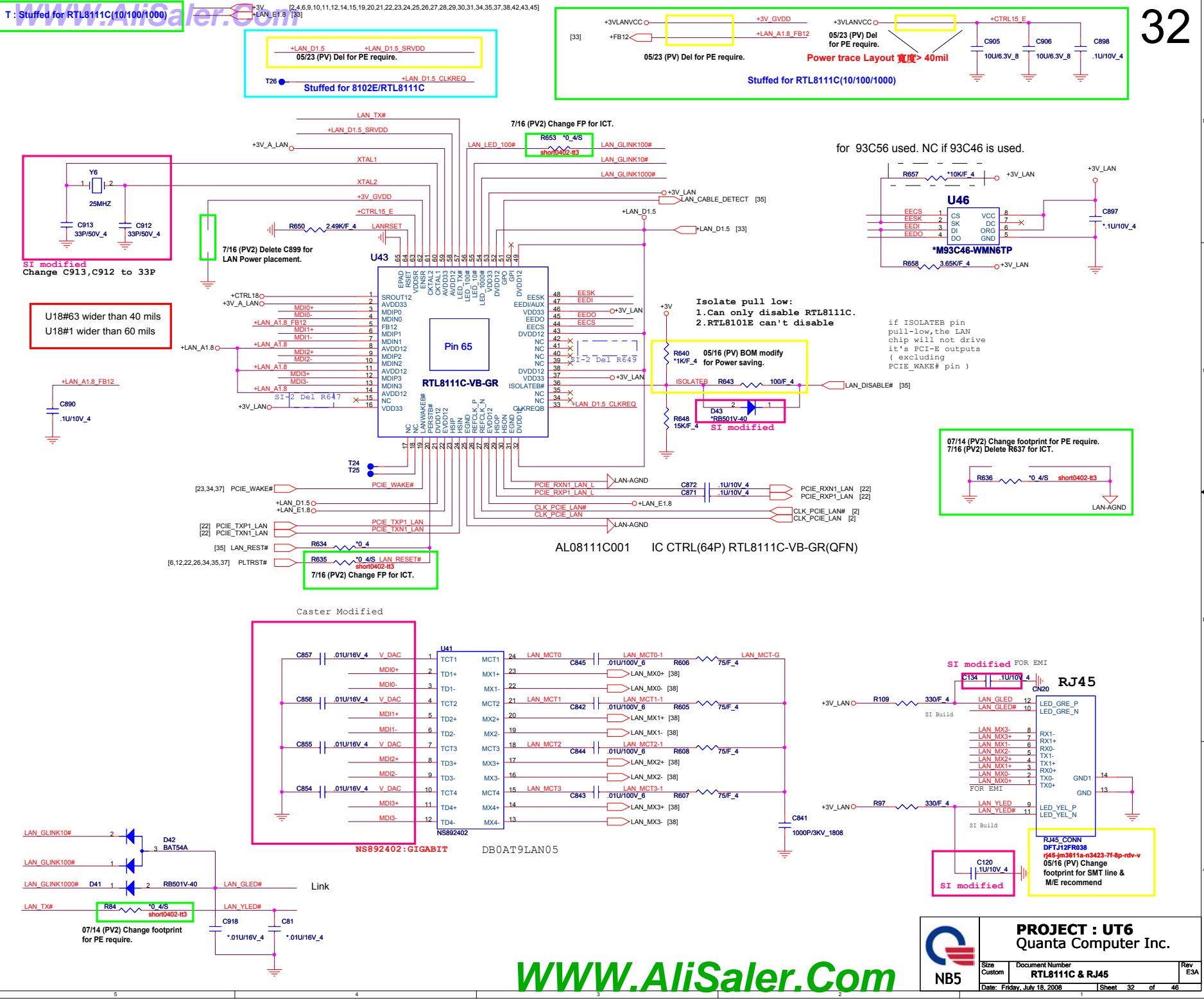


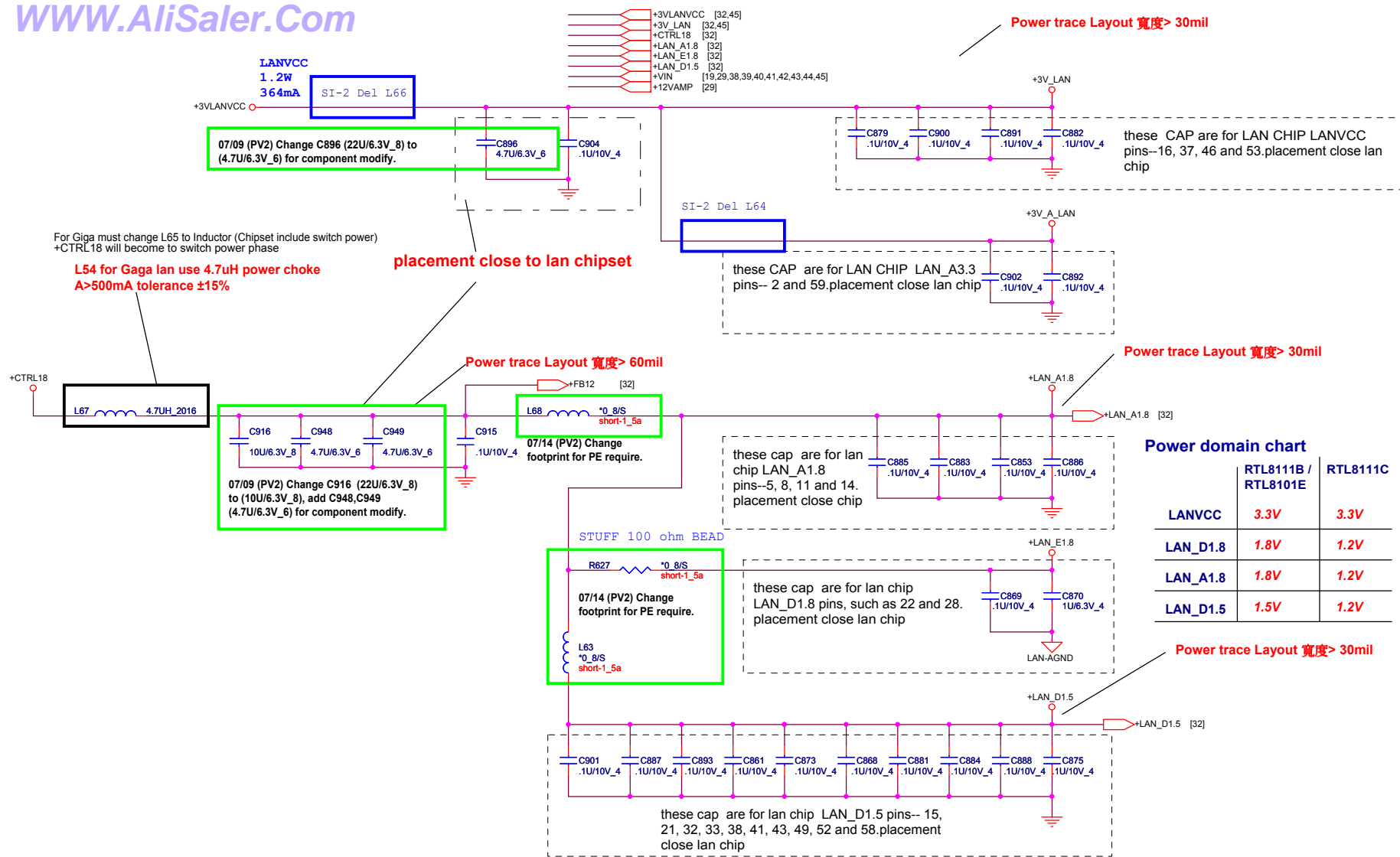
USB fingerprint CON



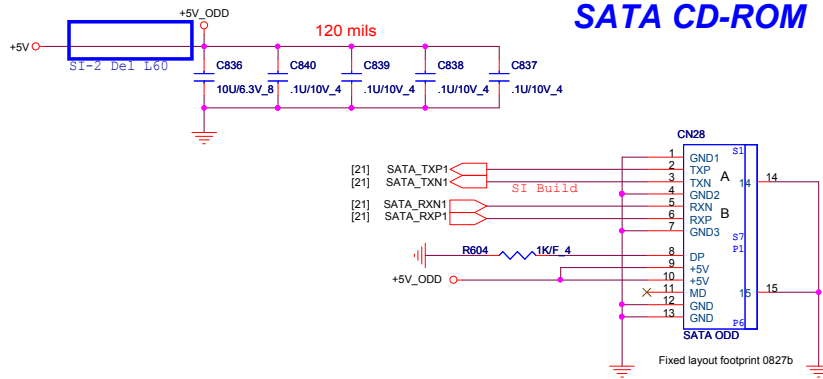
1. ESD GND
2. SYSTEM GND
3. USB-
4. USB+
5. USB PWR(+3V)



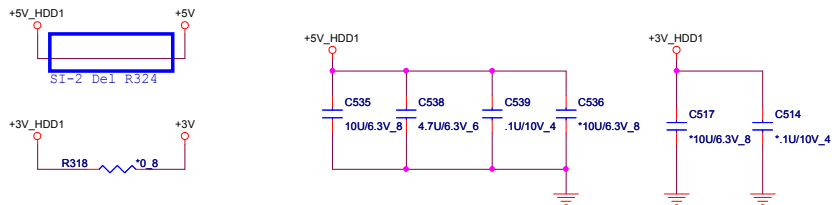
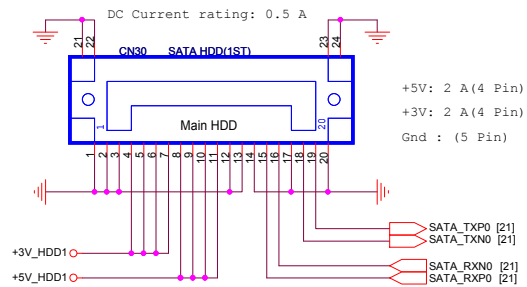




SATA CD-ROM



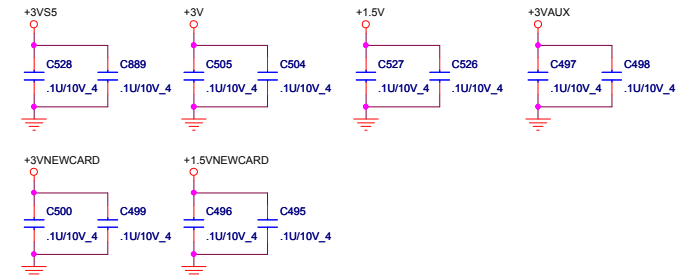
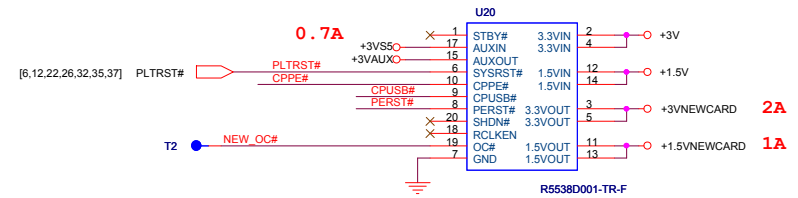
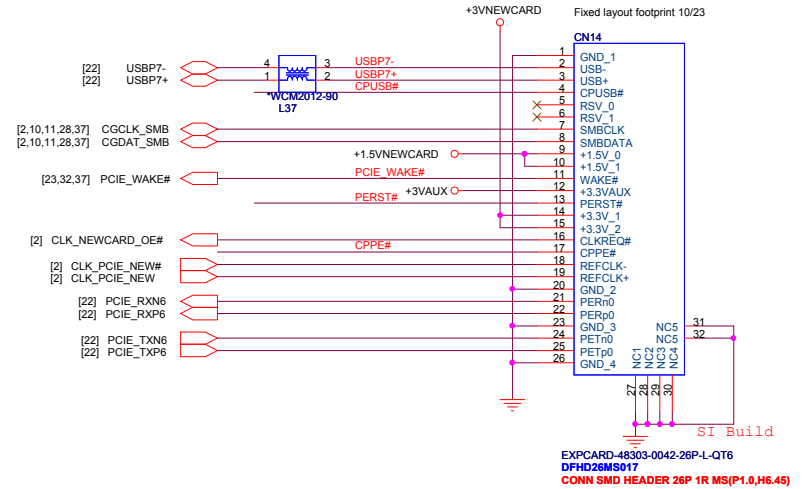
SATA_1 CONNECTOR




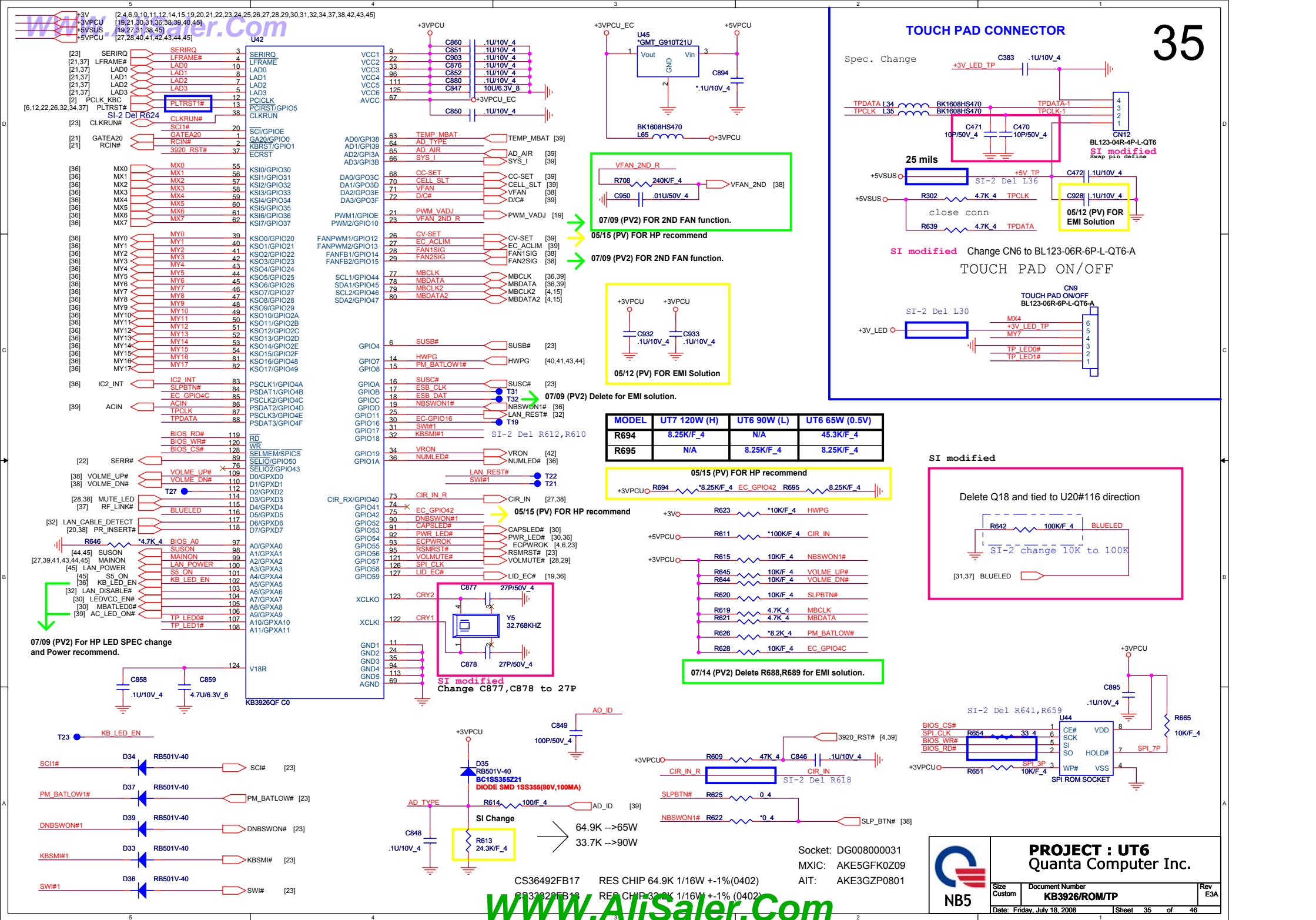
NEWCARD

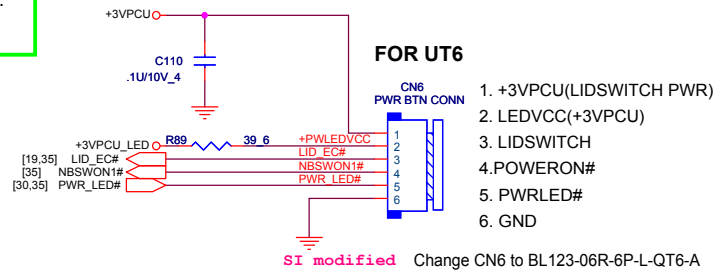
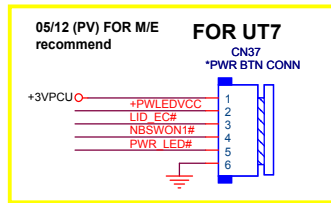
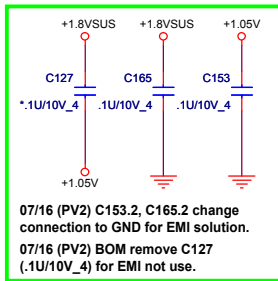
NEWCARD (PCIEXPRESS*1 + USB*1)

34



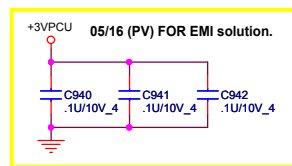
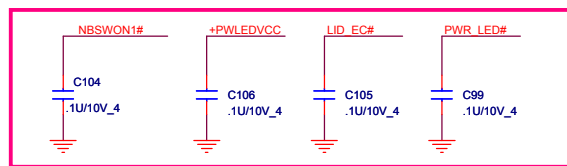
	PROJECT : UT6 Quanta Computer Inc.		
	Size Custom Document Number ODD/HDD/NEW CARD	Date: Friday, July 18, 2008	Rev E3A
Sheet 34 of 46		Date: Friday, July 18, 2008	





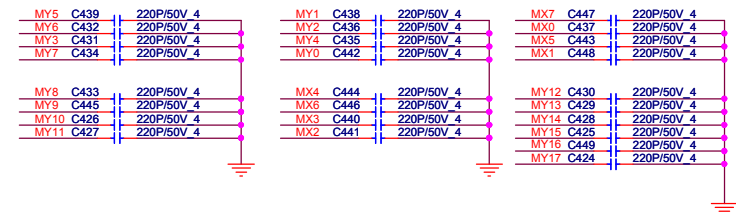
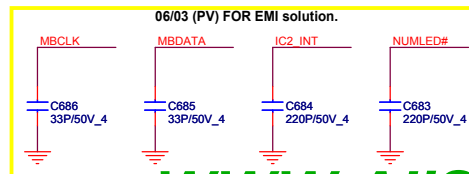
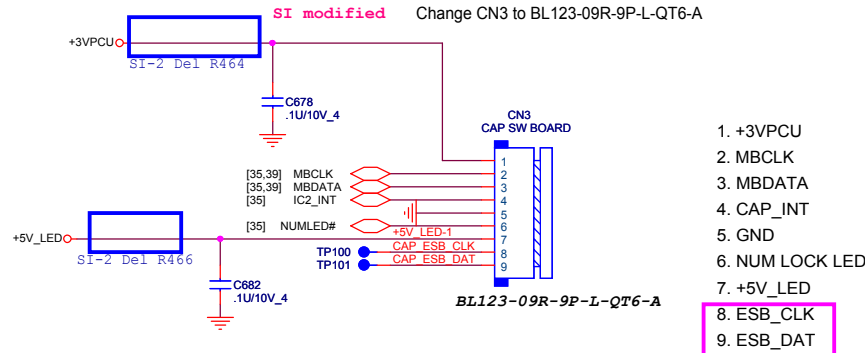
POWER BOTTOM CONNECT

SI modified For EMI

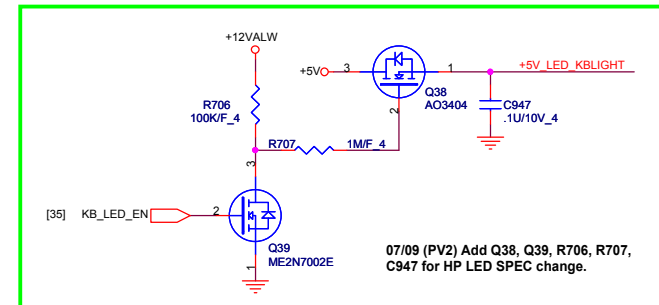
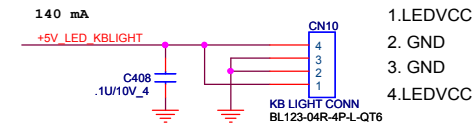
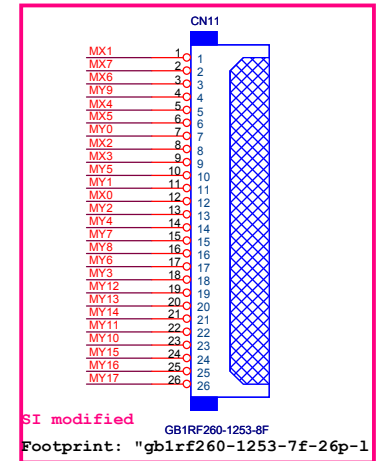
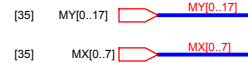
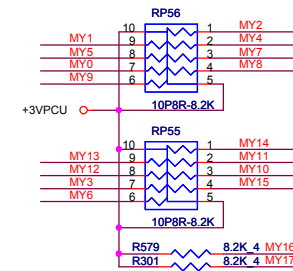


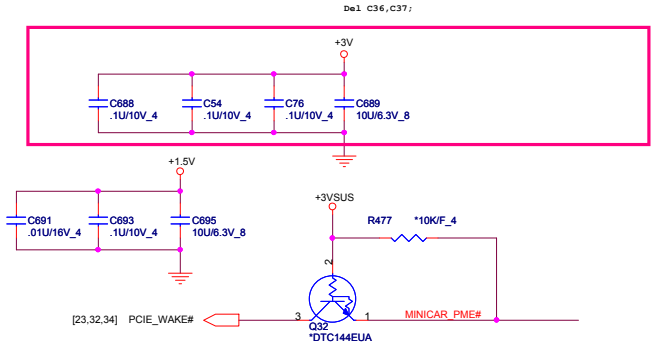
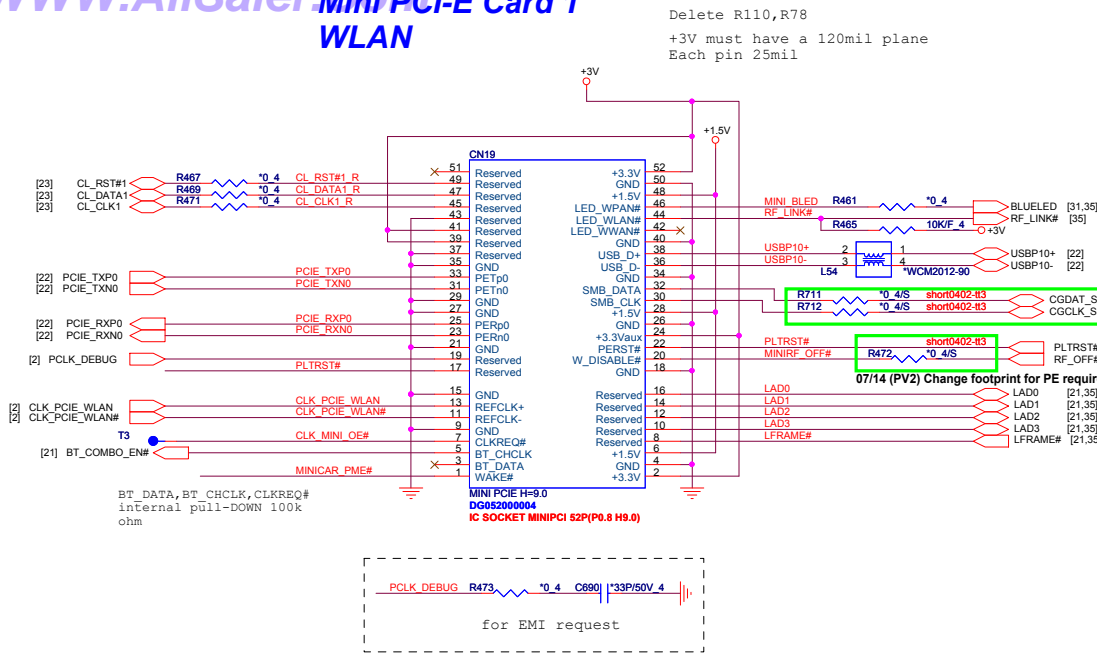
CAP SW CONNECT

Change CN3 to BL123-09R-9P-L-QT6-A



KEYBOARD PULL-UP

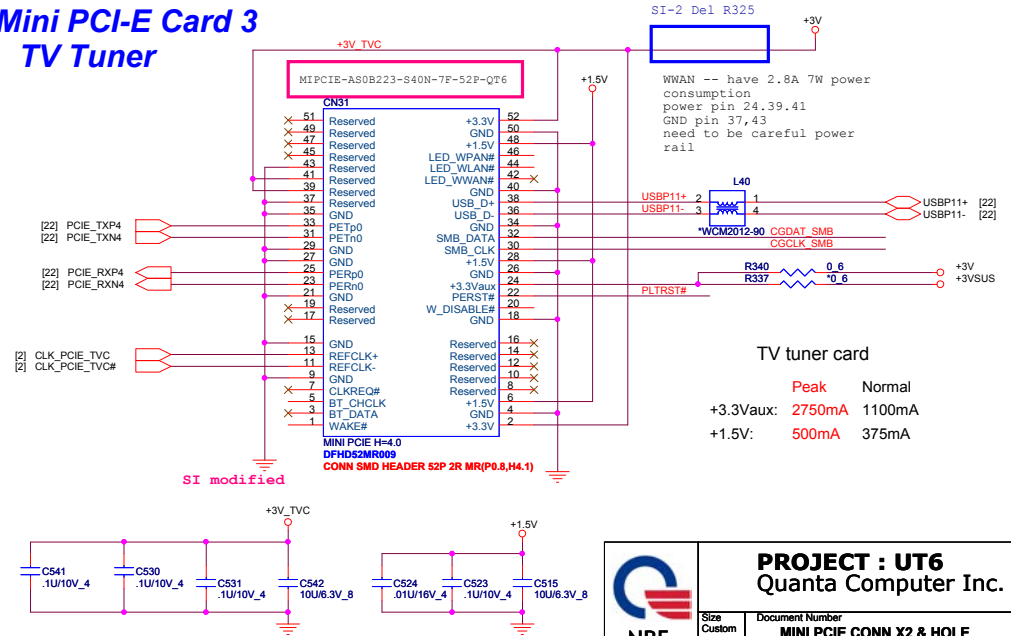


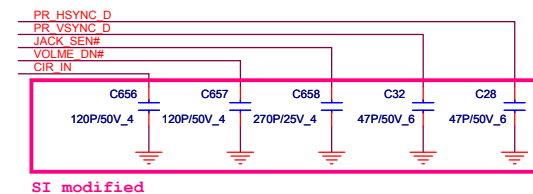
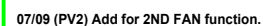
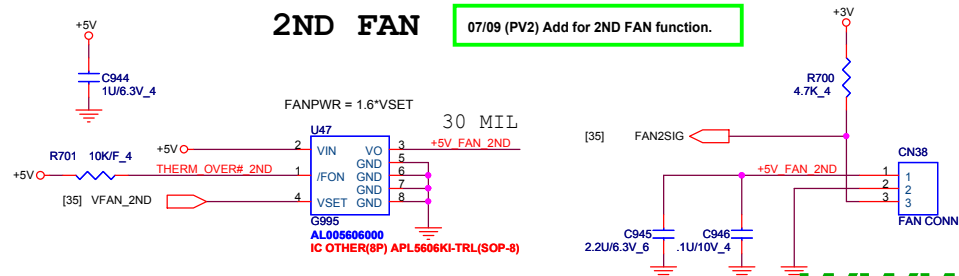
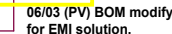
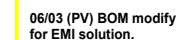
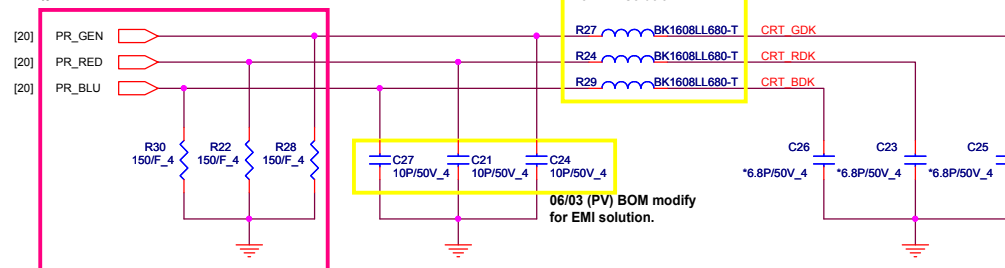
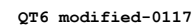
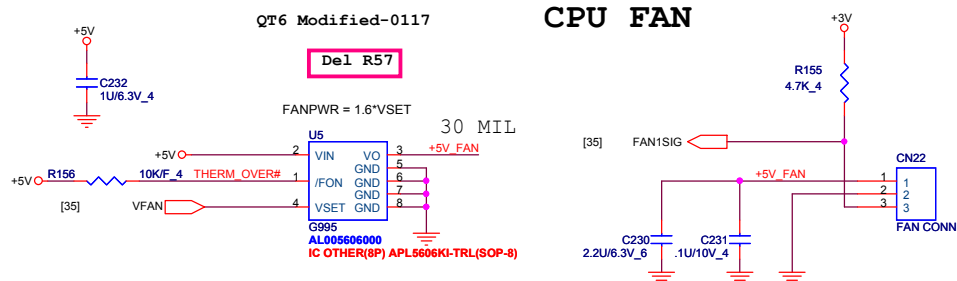
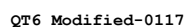
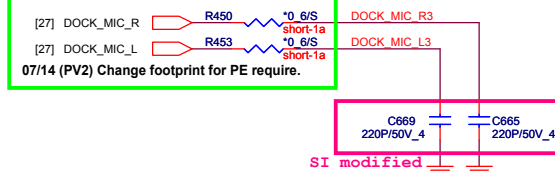
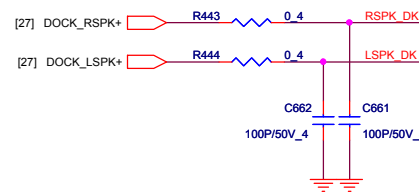
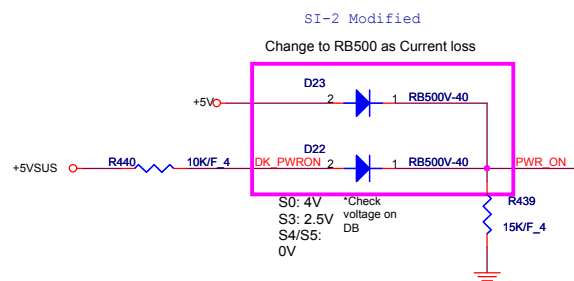
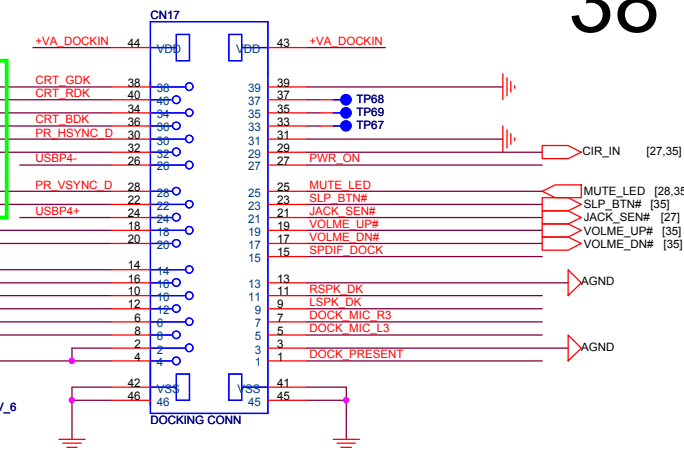
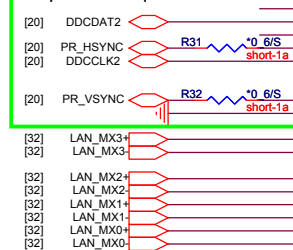
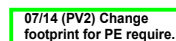
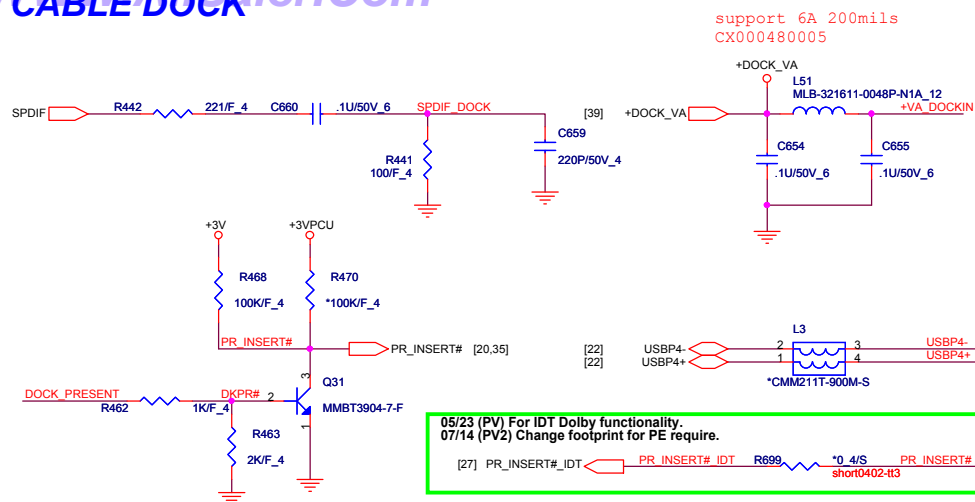


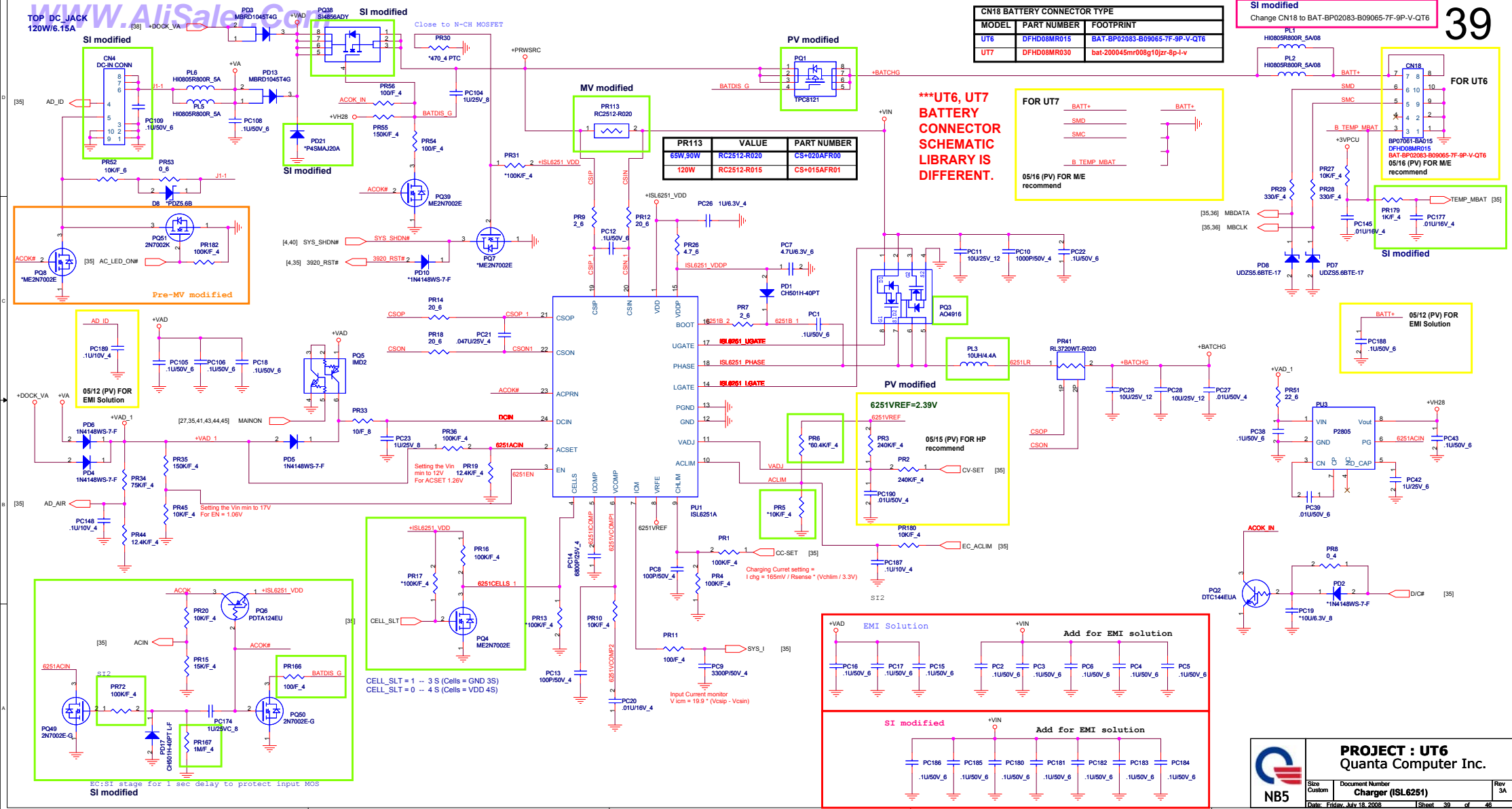
Mini PCI-E Card 2 ROBSON

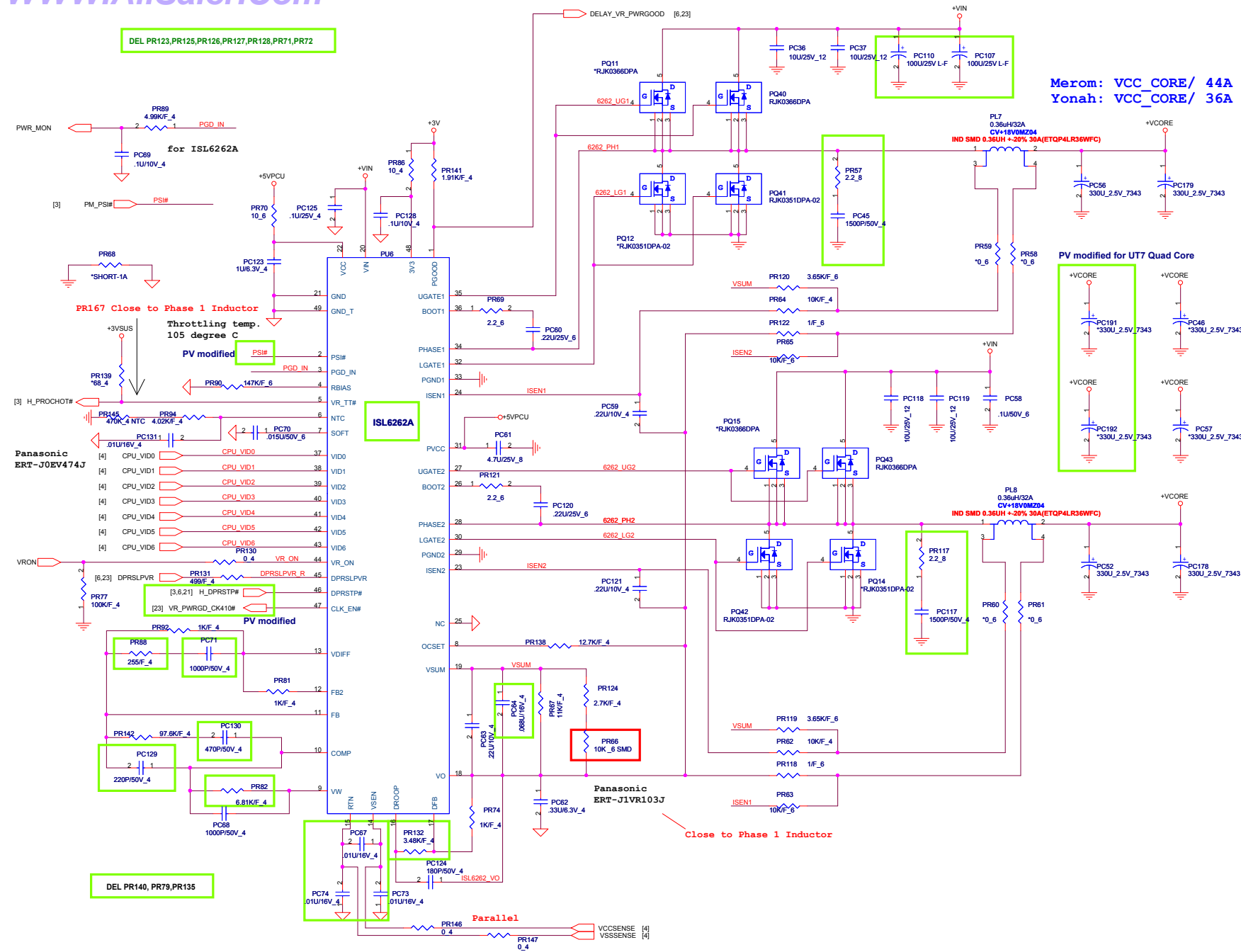
07/09 (PV2) Delete for no support ROBSON card.

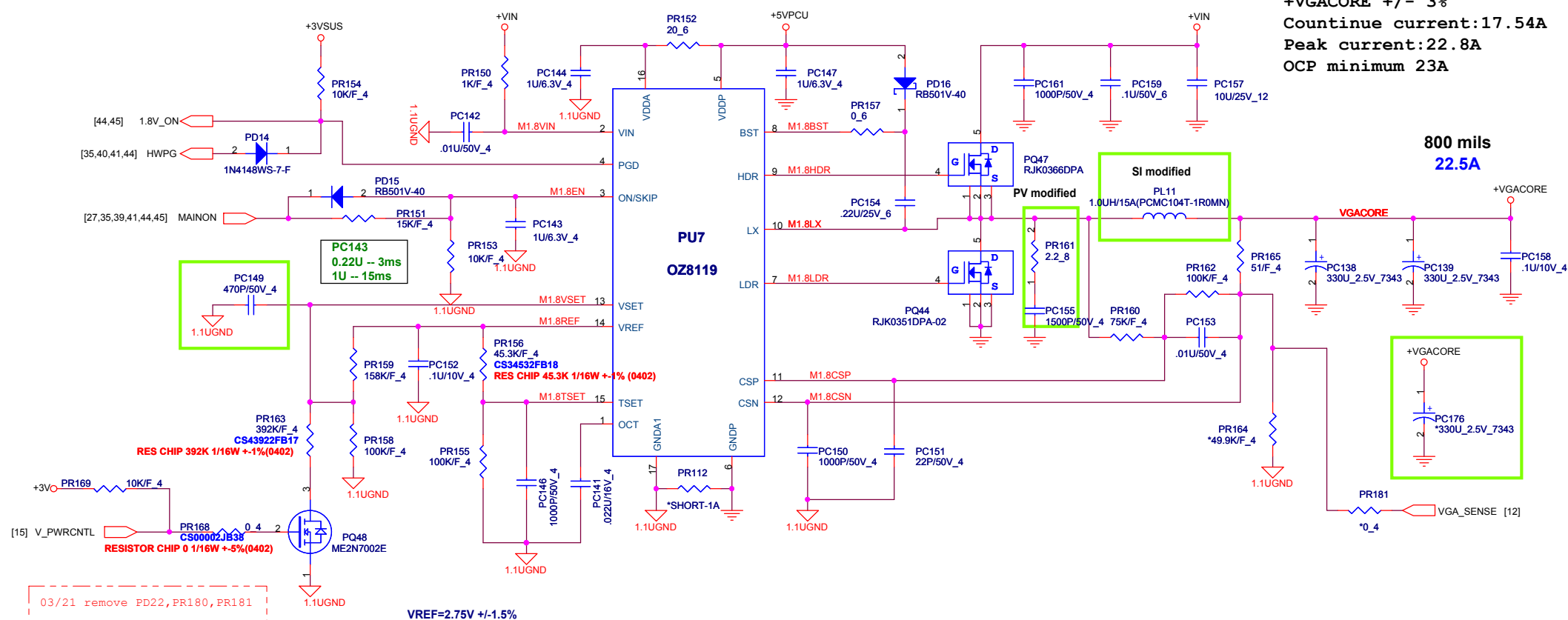
Mini PCI-E Card 3 TV Tuner











+VGACORE +/- 3%
Countinue current:17.54A
Peak current:22.8A
OCP minimum 23A

800 mils
22.5A

NB9P-GS: PR163=392Kohm
Output = 0.9V

NB9M-GE: PR203=590Kohm
NB9P-GS: PR203=768Kohm

CS45902FB10 RES CHIP 590K 1/16W +-1%(0402)
CS47682FB10 RES CHIP 768K 1/16W +-1%(0402)

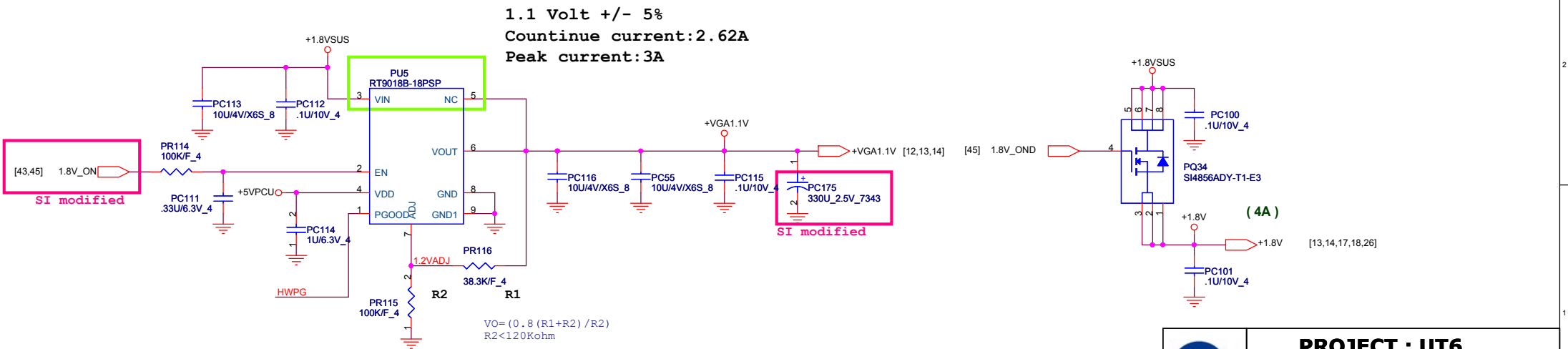
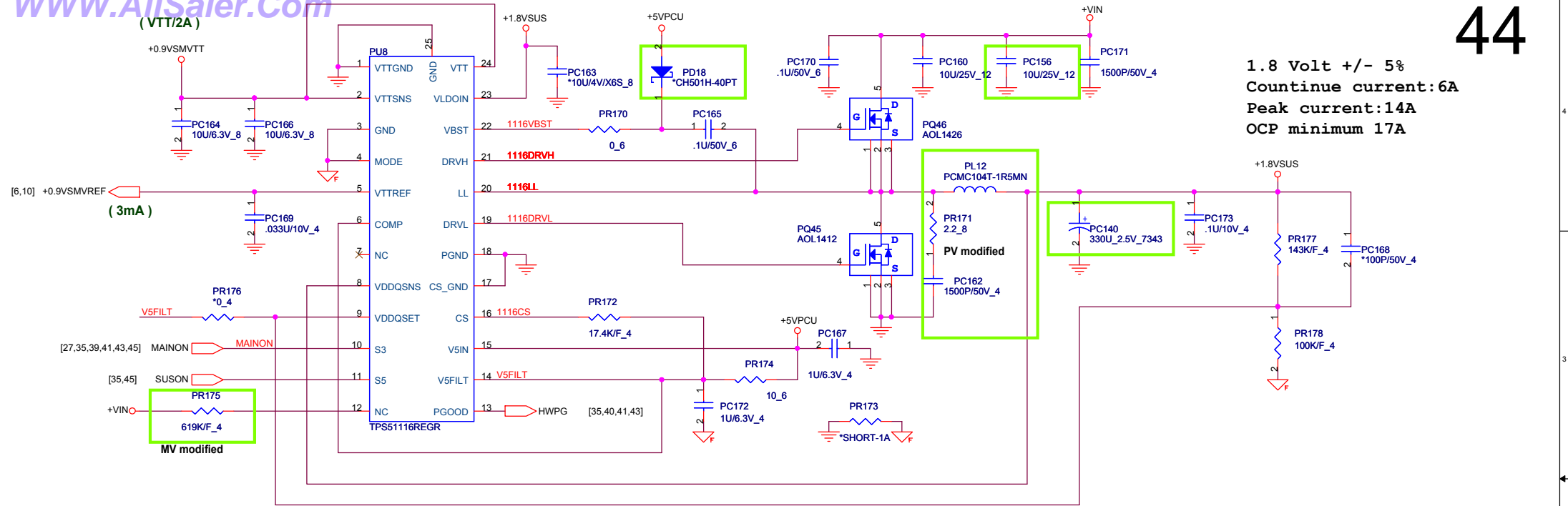
V_PWRCNTL	NB9P-GS
GPIOS	
Low	1.05V
High	0.9V

VGA_GPIO6	V_PWRCNTL		NB9P-GS	NB9M-GS
GPIO6	GPIO5			
Low	Low	MAX BAT	0.9V	0.9V
Low	High	SD DVD	0.9V	0.9V
High	Low	HD DVD	0.9V	0.9V
High	High	MAX PERF	1.05V	1.09V



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Size B	Document Number	Rev 3A
	1.8V/DDR_VTER/+1.8v/+1.1V	
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		AC MODE				DC MODE			
	Voltage level	S0	S3	S4	S5	S0	S3	S4	S5
+3VPCU	3.3V +/- 5%	V	V	V	V	V	V	V	V
+5VPCU	5V +/- 5%	V	V	V	V	V	V	V	V
+3VRTC	3.3V +/- 5%	V	V	V	V	V	V	V	V
+3VS5	3.3V +/- 5%	V	V	V	V	V	V		
+5VS5	5V +/- 5%	V	V	V	V	V	V		
+3VSUS	3.3V +/- 5%	V	V			V	V		
+5VSUS	5V +/- 5%	V	V			V	V		
+1.8VSUS	1.8V +/- 5%	V	V			V	V		
+0.9VSMVTT	0.9V +/- 5%	V	V			V	V		
+1.5V	1.5V +/- 5%	V				V			
+1.05V	1.05V +/- 5%	V				V			
+VCORE	0.9~1.15V	V				V			
+VGA_CORE	0.9~1.2V	V				V			
+VGA1.1V	1.1V +/- 5%	V				V			
+1.8V	1.8V +/- 5%	V				V			
+3VLAVCC	3.3V +/- 5%	V				V			



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Size
Custom

Document Number
Voltage

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E3A

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